



STEIF POWER  
TECHNOLOGY

40V N-Channel MOSFETs

**SPS4910**

## General Description

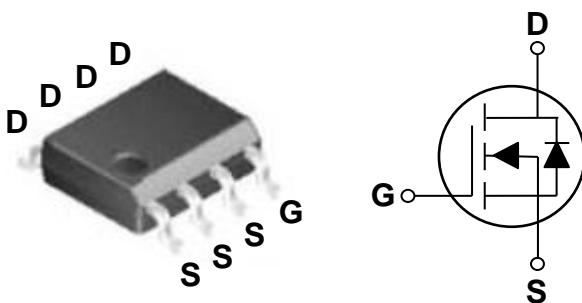
These N-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

BVDSS	RDS(ON)	ID
40V	19mΩ	6.7A

## Features

- 40V, 6.7A,  $RDS(ON)=19m\Omega @ VGS = 10V$
- Improved dv/dt capability
- Fast switching
- Green Device Available

## SOP8 Pin Configuration



## Applications

- Notebook
- Load Switch
- LED applications
- Hand-Held Device



## Absolute Maximum Ratings $T_c=25^\circ C$ unless otherwise noted

Symbol	Parameter	Rating	Units
$V_{DS}$	Drain-Source Voltage	40	V
$V_{GS}$	Gate-Source Voltage	$\pm 20$	V
$I_D$	Drain Current – Continuous ( $T_A=25^\circ C$ )	6.7	A
	Drain Current – Continuous ( $T_A=70^\circ C$ )	5.4	A
$I_{DM}$	Drain Current – Pulsed <sup>1</sup>	26.8	A
$P_D$	Power Dissipation ( $T_A=25^\circ C$ )	1.47	W
	Power Dissipation – Derate above $25^\circ C$	0.12	W/ $^\circ C$
$T_{STG}$	Storage Temperature Range	-55 to 150	$^\circ C$
$T_J$	Operating Junction Temperature Range	-55 to 150	$^\circ C$

## Thermal Characteristics

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction to ambient	---	85	$^\circ C/W$



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### Electrical Characteristics ( $T_J=25^\circ\text{C}$ , unless otherwise noted)

#### Off Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$\text{BV}_{\text{DSS}}$	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}$ , $I_D=250\mu\text{A}$	40	---	---	V
$\Delta \text{BV}_{\text{DSS}}/\Delta T_J$	$\text{BV}_{\text{DSS}}$ Temperature Coefficient	Reference to $25^\circ\text{C}$ , $I_D=1\text{mA}$	---	0.034	---	$\text{V}^\circ\text{C}$
$I_{\text{DSS}}$	Drain-Source Leakage Current	$V_{\text{DS}}=40\text{V}$ , $V_{\text{GS}}=0\text{V}$ , $T_J=25^\circ\text{C}$	---	---	1	$\mu\text{A}$
		$V_{\text{DS}}=32\text{V}$ , $V_{\text{GS}}=0\text{V}$ , $T_J=85^\circ\text{C}$	---	---	10	$\mu\text{A}$
$I_{\text{GSS}}$	Gate-Source Leakage Current	$V_{\text{GS}}=\pm 20\text{V}$ , $V_{\text{DS}}=0\text{V}$	---	---	$\pm 100$	nA

#### On Characteristics

$R_{\text{DS(ON)}}$	Static Drain-Source On-Resistance	$V_{\text{GS}}=10\text{V}$ , $I_D=6\text{A}$	---	16	19	$\text{m}\Omega$
		$V_{\text{GS}}=4.5\text{V}$ , $I_D=4\text{A}$	---	20	25	$\text{m}\Omega$
$V_{\text{GS(th)}}$	Gate Threshold Voltage	$V_{\text{GS}}=V_{\text{DS}}$ , $I_D = 250\mu\text{A}$	1.2	1.5	2.5	V
			---	-4.3	---	$\text{mV}^\circ\text{C}$
$g_{\text{fs}}$	Forward Transconductance	$V_{\text{DS}}=10\text{V}$ , $I_D=3\text{A}$	---	6	---	S

#### Dynamic and switching Characteristics

$Q_g$	Total Gate Charge <sup>2, 3</sup>	$V_{\text{DS}}=32\text{V}$ , $V_{\text{GS}}=10\text{V}$ , $I_D=6\text{A}$	---	11.8	23	nC
$Q_{\text{gs}}$	Gate-Source Charge <sup>2, 3</sup>		---	1.7	3.4	
$Q_{\text{gd}}$	Gate-Drain Charge <sup>2, 3</sup>		---	4	8	
$T_{\text{d(on)}}$	Turn-On Delay Time <sup>2, 3</sup>	$V_{\text{DD}}=20\text{V}$ , $V_{\text{GS}}=10\text{V}$ , $R_G=3.3\Omega$	---	5	10	ns
$T_r$	Rise Time <sup>2, 3</sup>		---	8	16	
$T_{\text{d(off)}}$	Turn-Off Delay Time <sup>2, 3</sup>		---	17	34	
$T_f$	Fall Time <sup>2, 3</sup>		---	5	10	
$C_{\text{iss}}$	Input Capacitance	$V_{\text{DS}}=20\text{V}$ , $V_{\text{GS}}=0\text{V}$ , $F=1\text{MHz}$	---	722	1440	pF
$C_{\text{oss}}$	Output Capacitance		---	83	166	
$C_{\text{rss}}$	Reverse Transfer Capacitance		---	61	122	
$R_g$	Gate resistance	$V_{\text{GS}}=0\text{V}$ , $V_{\text{DS}}=0\text{V}$ , $F=1\text{MHz}$	---	2.1	---	$\Omega$

#### Drain-Source Diode Characteristics and Maximum Ratings

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$I_s$	Continuous Source Current	$V_G=V_D=0\text{V}$ , Force Current	---	---	6.7	A
			---	---	13.4	A
$V_{\text{SD}}$	Diode Forward Voltage	$V_{\text{GS}}=0\text{V}$ , $I_s=1\text{A}$ , $T_J=25^\circ\text{C}$	---	---	1	V

Note :

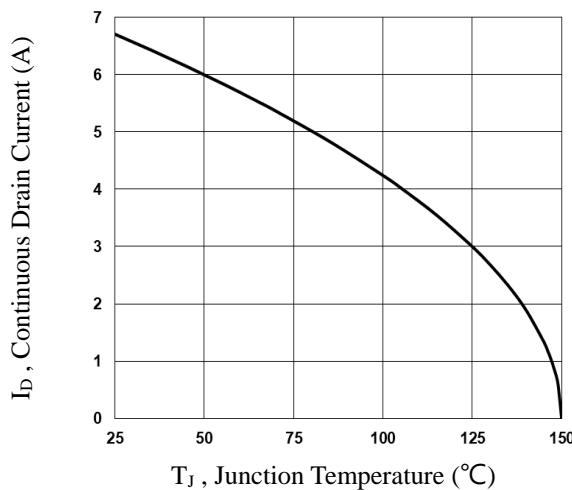
1. Repetitive Rating : Pulsed width limited by maximum junction temperature.
2. The data tested by pulsed , pulse width  $\leq 300\text{us}$  , duty cycle  $\leq 2\%$ .
3. Essentially independent of operating temperature.



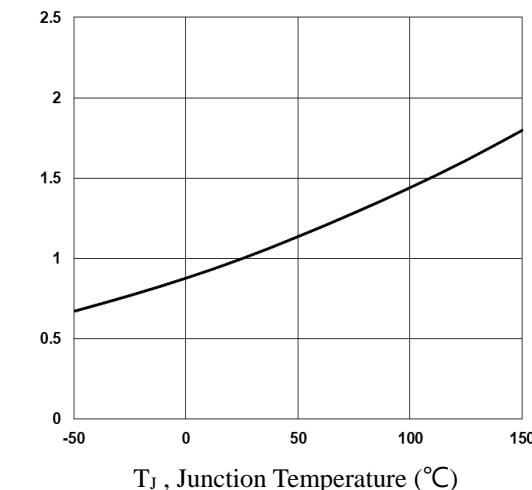
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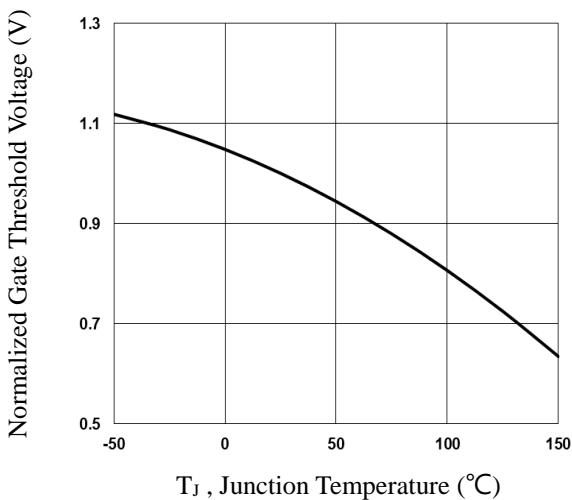
**SPS4910**



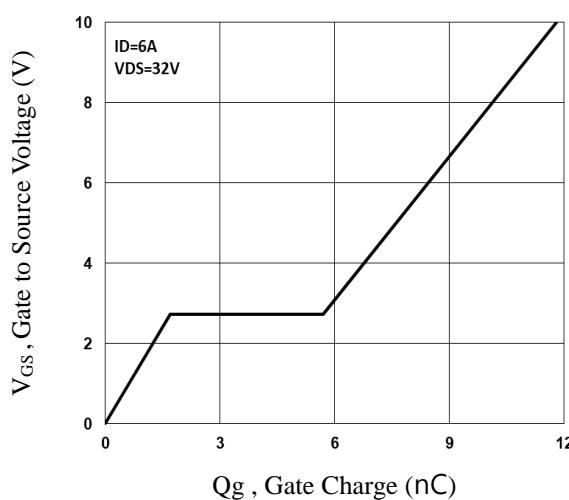
**Fig.1 Continuous Drain Current vs.  $T_J$**



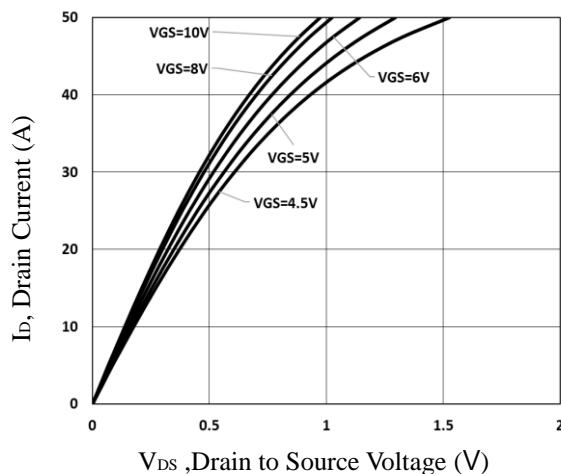
**Fig.2 Normalized  $R_{DS(ON)}$  vs.  $T_J$**



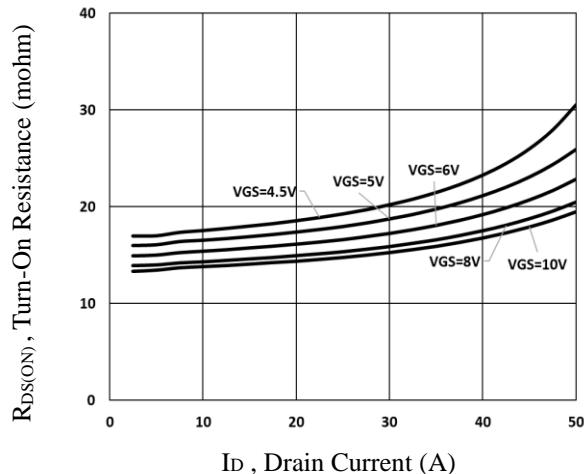
**Fig.3 Normalized  $V_{th}$  vs.  $T_J$**



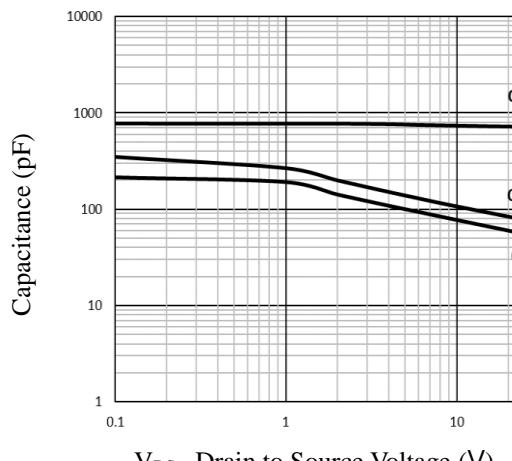
**Fig.4 Gate Charge Waveform**



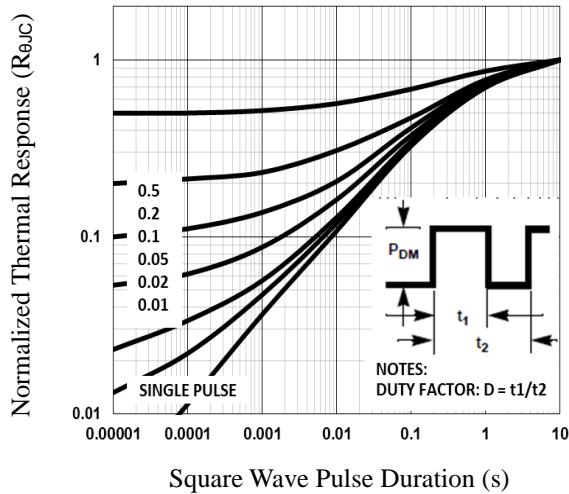
**Fig.5 Typical Output Characteristics**



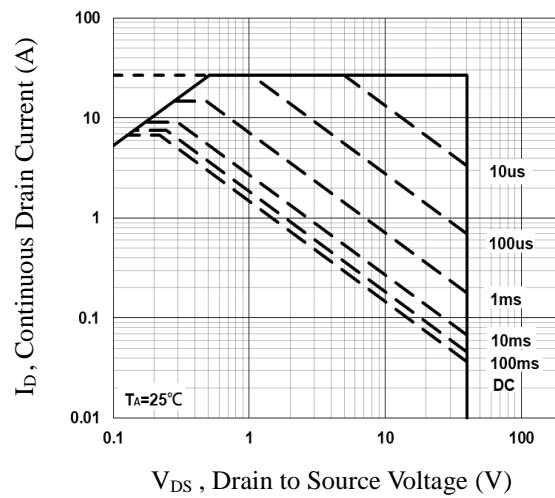
**Fig.6 Turn-On Resistance vs.  $I_D$**



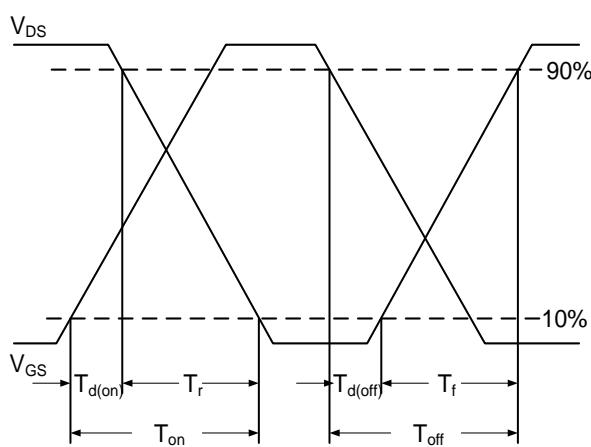
**Fig.7 Capacitance Characteristics**



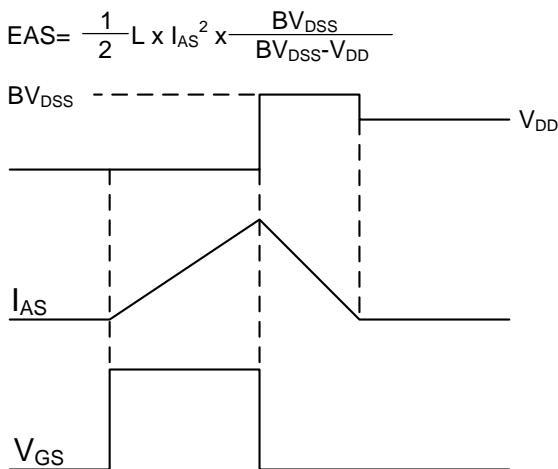
**Fig.8 Normalized Transient Impedance**



**Fig.9 Maximum Safe Operation Area**



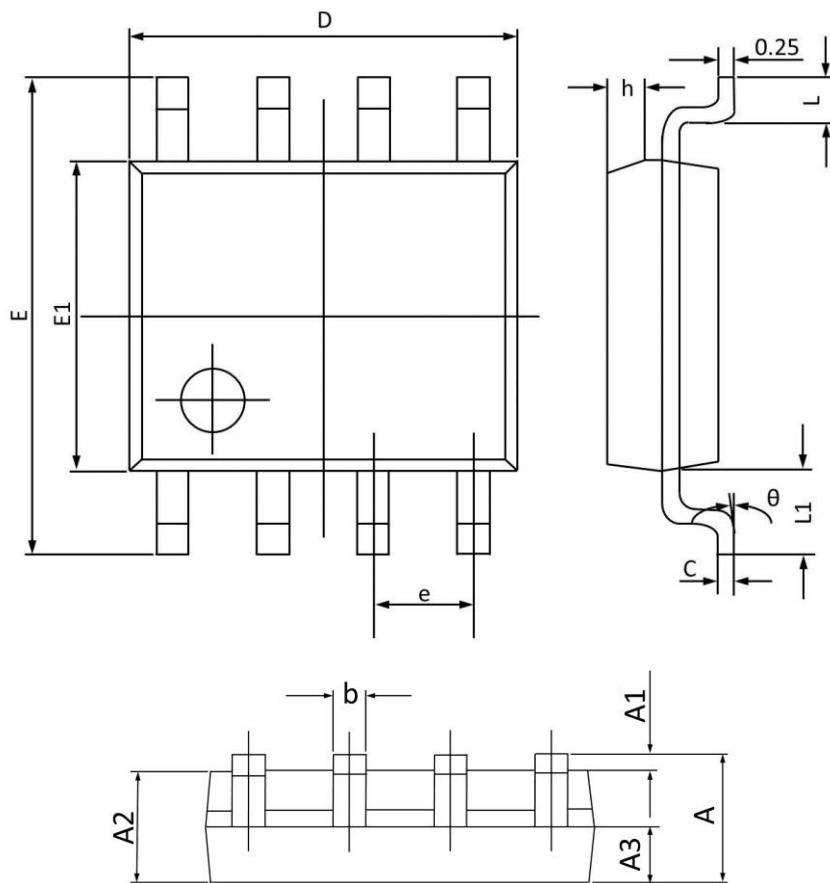
**Fig.10 Switching Time Waveform**



**Fig.11 EAS Waveform**



## SOP8 PACKAGE INFORMATION



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	1.350	1.750	0.053	0.068
A1	0.100	0.250	0.004	0.009
A2	1.300	1.500	0.052	0.059
A3	0.600	0.700	0.024	0.027
b	0.390	0.480	0.016	0.018
c	0.210	0.260	0.009	0.010
D	4.700	5.100	0.186	0.200
E	5.800	6.200	0.229	0.244
E1	3.700	4.100	0.146	0.161
e	1.270(BSC)		0.050(BSC)	
h	0.250	0.500	0.010	0.019
L	0.500	0.800	0.019	0.031
L1	1.050(BSC)		0.041(BSC)	
theta	0°	8°	0°	8°