

### General Description

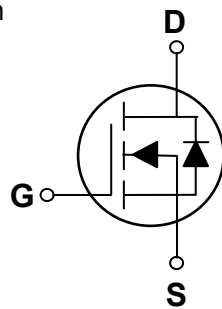
These N-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

BVDSS	RDSON	ID
150V	13mΩ	80A

### Features

- 150V,80A,  $R_{DS(ON)} = 13m\Omega @ V_{GS} = 10V$
- Improved dv/dt capability
- Fast switching
- 100% EAS Guaranteed
- Green Device Available

### TO220 Pin Configuration



### Applications

- Networking
- Load Switch
- LED applications
- Quick Charger



### Absolute Maximum Ratings $T_c=25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter	Rating	Units
$V_{DS}$	Drain-Source Voltage	150	V
$V_{GS}$	Gate-Source Voltage	$\pm 20$	V
$I_D$	Drain Current – Continuous ( $T_c=25^\circ\text{C}$ ) (Chip Limitation)	80	A
	Drain Current – Continuous ( $T_c=100^\circ\text{C}$ ) (Chip Limitation)	50	A
$I_{DM}$	Drain Current – Pulsed <sup>1</sup>	320	A
EAS	Single Pulse Avalanche Energy <sup>2</sup>	280	mJ
IAS	Single Pulse Avalanche Current <sup>2</sup>	75	A
$P_D$	Power Dissipation ( $T_c=25^\circ\text{C}$ )	208	W
	Power Dissipation – Derate above $25^\circ\text{C}$	1.66	W/ $^\circ\text{C}$
$T_{STG}$	Storage Temperature Range	-50 to 150	$^\circ\text{C}$
$T_J$	Operating Junction Temperature Range	-50 to 150	$^\circ\text{C}$

### Thermal Characteristics

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction to ambient	---	62	$^\circ\text{C}/\text{W}$
$R_{\theta JC}$	Thermal Resistance Junction to Case	---	0.6	$^\circ\text{C}/\text{W}$

**Electrical Characteristics ( $T_J=25^\circ\text{C}$ , unless otherwise noted)**
**Off Characteristics**

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$BV_{DSS}$	Drain-Source Breakdown Voltage	$V_{GS}=0V, I_D=250\mu A$	150	---	---	V
$\Delta BV_{DSS}/\Delta T_J$	$BV_{DSS}$ Temperature Coefficient	Reference to $25^\circ\text{C}$ , $I_D=1\text{mA}$	---	0.22	---	$V/^\circ\text{C}$
$I_{DSS}$	Drain-Source Leakage Current	$V_{DS}=150V, V_{GS}=0V, T_J=25^\circ\text{C}$	---	---	1	$\mu A$
		$V_{DS}=120V, V_{GS}=0V, T_J=125^\circ\text{C}$	---	---	10	$\mu A$
$I_{GSS}$	Gate-Source Leakage Current	$V_{GS}=\pm 20V, V_{DS}=0V$	---	---	$\pm 100$	nA

**On Characteristics**

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$R_{DS(ON)}$	Static Drain-Source On-Resistance	$V_{GS}=10V, I_D=15A$	---	10.5	13	$m\Omega$
$V_{GS(th)}$	Gate Threshold Voltage	$V_{GS}=V_{DS}, I_D=250\mu A$	1.5	2.3	3.5	V
$\Delta V_{GS(th)}$	$V_{GS(th)}$ Temperature Coefficient		---	-4	---	$mV/^\circ\text{C}$
$g_{fs}$	Forward Transconductance	$V_{DS}=3V, I_D=10A$	---	15	---	S

**Dynamic and switching Characteristics**

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$Q_g$	Total Gate Charge <sup>3,4</sup>	$V_{DS}=100V, V_{GS}=10V, I_D=10A$	---	222	450	nC
$Q_{gs}$	Gate-Source Charge <sup>3,4</sup>		---	66	120	
$Q_{gd}$	Gate-Drain Charge <sup>3,4</sup>		---	49	100	
$T_{d(on)}$	Turn-On Delay Time <sup>3,4</sup>	$V_{DD}=50V, V_{GS}=10V, R_G=6\Omega$ $I_D=1A$	---	60.2	120	ns
$T_r$	Rise Time <sup>3,4</sup>		---	65.6	130	
$T_{d(off)}$	Turn-Off Delay Time <sup>3,4</sup>		---	198	400	
$T_f$	Fall Time <sup>3,4</sup>		---	84	170	
$C_{iss}$	Input Capacitance	$V_{DS}=25V, V_{GS}=0V, F=1\text{MHz}$	---	15100	22000	pF
$C_{oss}$	Output Capacitance		---	690	900	
$C_{riss}$	Reverse Transfer Capacitance		---	86	70	
$R_g$	Gate resistance	$V_{GS}=0V, V_{DS}=0V, F=1\text{MHz}$	---	2	4	$\Omega$

**Drain-Source Diode Characteristics and Maximum Ratings**

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$I_S$	Continuous Source Current	$V_G=V_D=0V$ , Force Current	---	---	80	A
$I_{SM}$	Pulsed Source Current		---	---	160	A
$V_{SD}$	Diode Forward Voltage	$V_{GS}=0V, I_S=1A, T_J=25^\circ\text{C}$	---	---	1	V

**Note :**

1. Repetitive Rating : Pulsed width limited by maximum junction temperature.
2.  $V_{DD}=50V, V_{GS}=10V, L=0.1\text{mH}, I_{AS}=75A, R_G=25\Omega$ , Starting  $T_J=25^\circ\text{C}$ .
3. The data tested by pulsed, pulse width  $\leq 300\mu s$ , duty cycle  $\leq 2\%$ .
4. Essentially independent of operating temperature.

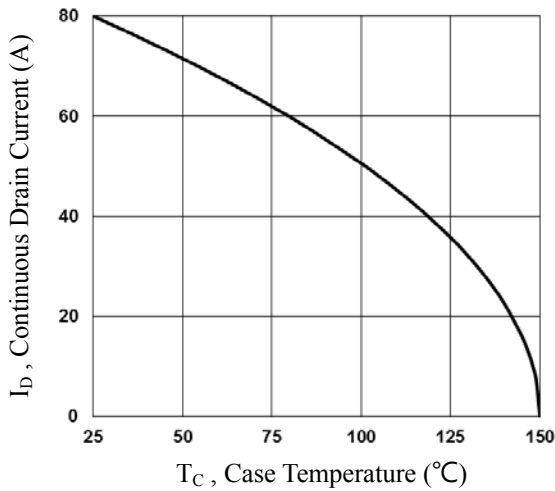


Fig.1 Continuous Drain Current vs.  $T_C$

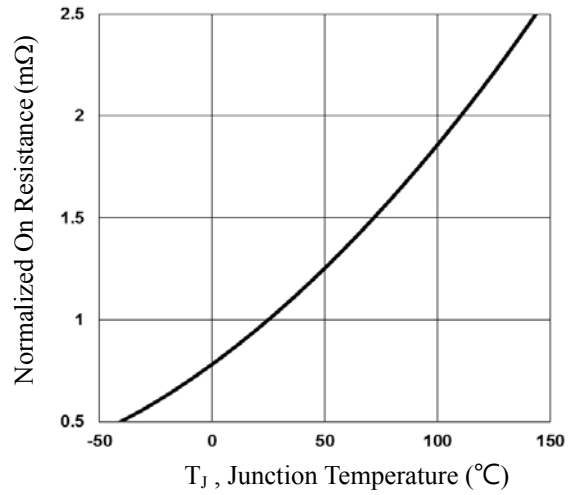


Fig.2 Normalized  $R_{DS(on)}$  vs.  $T_J$

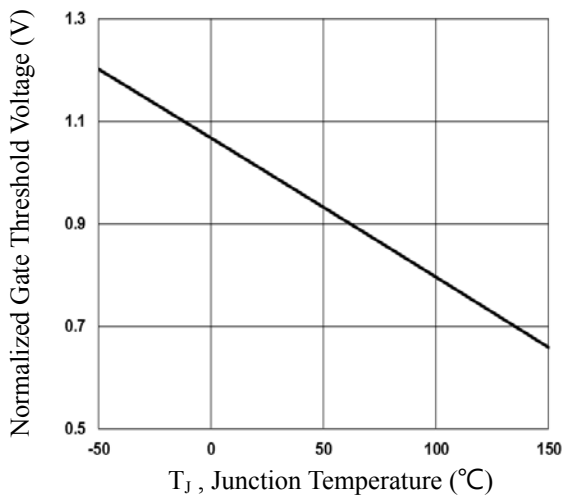


Fig.3 Normalized  $V_{th}$  vs.  $T_J$

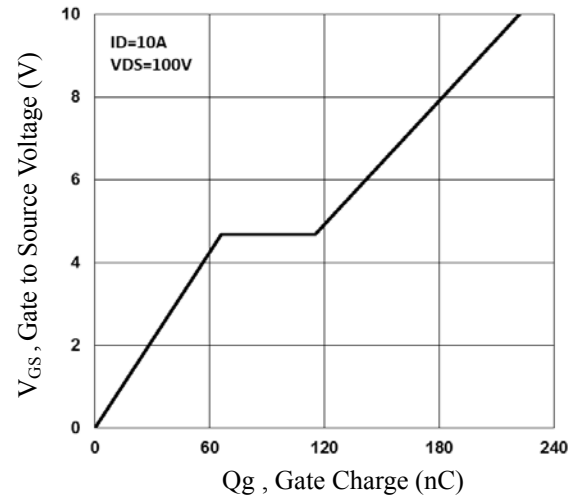


Fig.4 Gate Charge Characteristics

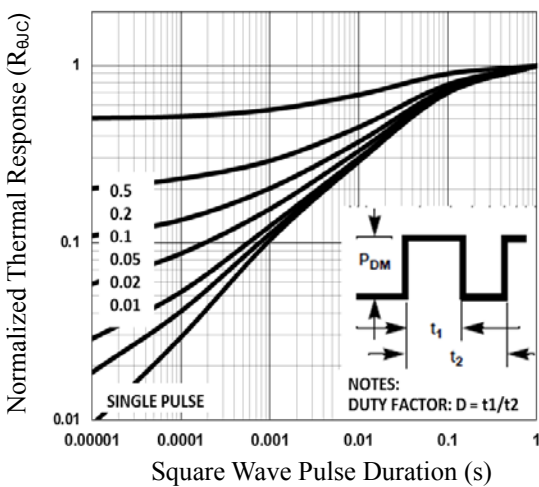


Fig.5 Normalized Transient Impedance

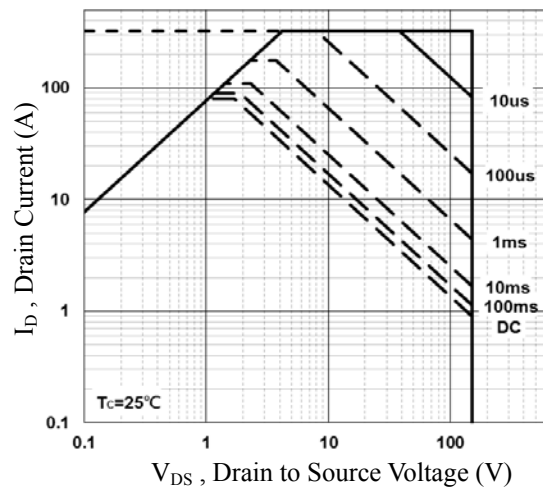


Fig.6 Maximum Safe Operation Area

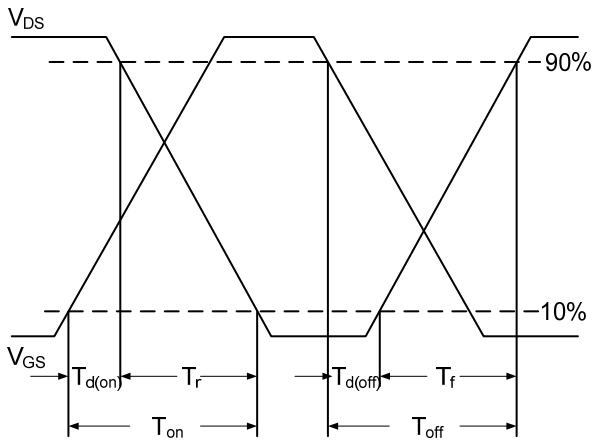


Fig.7 Switching Time Waveform

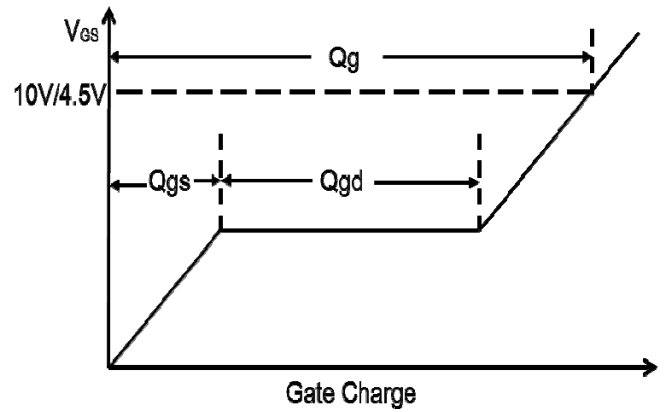
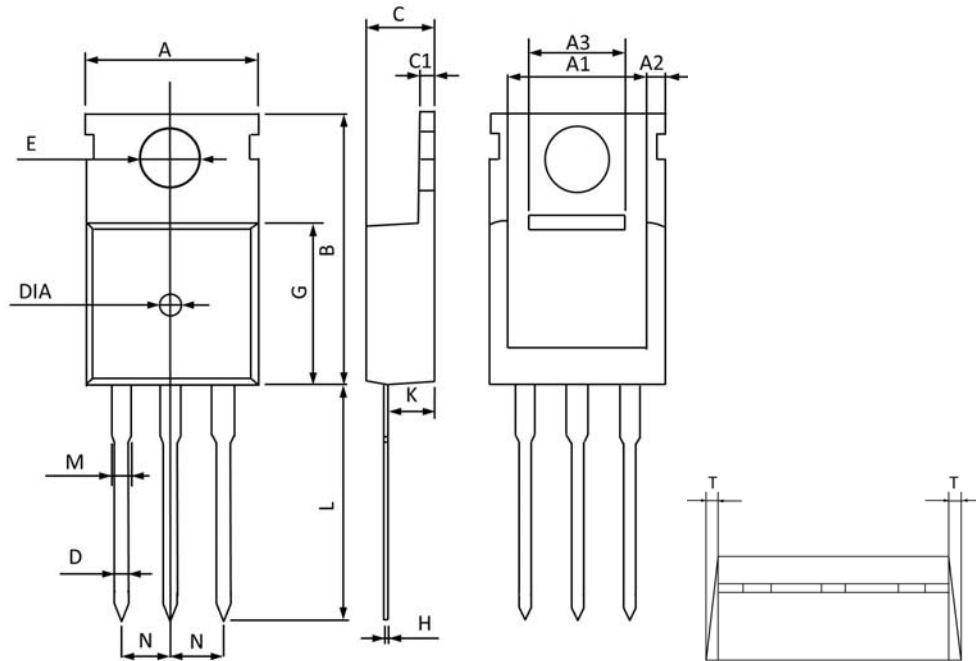


Fig.8 Gate Charge Waveform



TO220 PACKAGE INFORMATION



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	MAX	MIN	MAX	MIN
A	10.300	9.700	0.406	0.382
A1	8.840	8.440	0.348	0.332
A2	1.250	1.050	0.049	0.041
A3	5.300	5.100	0.209	0.201
B	16.200	15.400	0.638	0.606
C	4.680	4.280	0.184	0.169
C1	1.500	1.100	0.059	0.043
D	1.000	0.600	0.039	0.024
E	3.800	3.400	0.150	0.134
G	9.300	8.700	0.366	0.343
H	0.600	0.400	0.024	0.016
K	2.700	2.100	0.106	0.083
L	13.600	12.800	0.535	0.504
M	1.500	1.100	0.059	0.043
N	2.590	2.490	0.102	0.098
T	W0.35		W0.014	
DIA	Φ1.5 TYP.	deep0.2 TYP.	Φ0.059 TYP.	deep0.008 TYP.