



STEIF POWER
TECHNOLOGY

200V N-Channel MOSFETs

SPP20N20

General Description

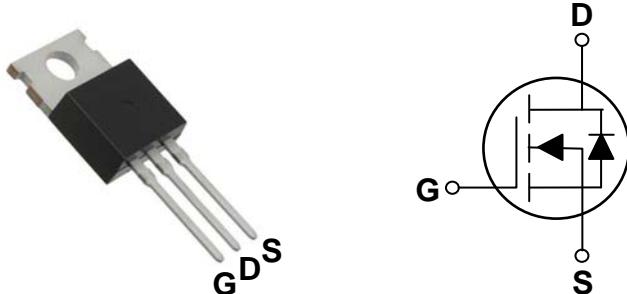
These N-Channel enhancement mode power field effect transistors are planar stripe, DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency switch mode power supply

BVDSS	RDSON	ID
200V	130mΩ	20A

Features

- Improved dv/dt capability
- Fast switching
- 100% EAS Guaranteed
- Green Device Available

TO220 Pin Configuration



Applications

- High efficient switched mode power supplies
- TV Power
- Adapter/charger
- Networking
- PV Inverter / UPS



Absolute Maximum Ratings $T_c=25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	200	V
V_{GS}	Gate-Source Voltage	± 30	V
I_D	Drain Current – Continuous ($T_c=25^\circ\text{C}$)	20	A
	Drain Current – Continuous ($T_c=100^\circ\text{C}$)	13	A
I_{DM}	Drain Current – Pulsed ¹	80	A
EAS	Single Pulse Avalanche Energy ²	420	mJ
IAS	Single Pulse Avalanche Current ²	29	A
P_D	Power Dissipation ($T_c=25^\circ\text{C}$)	83	W
	Power Dissipation – Derate above 25°C	0.66	W/ $^\circ\text{C}$
T_{STG}	Storage Temperature Range	-55 to 150	$^\circ\text{C}$
T_J	Operating Junction Temperature Range	-55 to 150	$^\circ\text{C}$

Thermal Characteristics

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction to ambient	---	62	$^\circ\text{C}/\text{W}$
$R_{\theta JC}$	Thermal Resistance Junction to Case	---	1.5	$^\circ\text{C}/\text{W}$

Electrical Characteristics ($T_J=25^\circ\text{C}$, unless otherwise noted)
Off Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}$, $I_D=250\mu\text{A}$	200	---	---	V
I_{DSS}	Drain-Source Leakage Current	$V_{\text{DS}}=200\text{V}$, $V_{\text{GS}}=0\text{V}$, $T_J=25^\circ\text{C}$	---	---	1	μA
		$V_{\text{DS}}=160\text{V}$, $V_{\text{GS}}=0\text{V}$, $T_J=125^\circ\text{C}$	---	---	10	μA
I_{GSS}	Gate-Source Leakage Current	$V_{\text{GS}}=\pm 30\text{V}$, $V_{\text{DS}}=0\text{V}$	---	---	± 100	nA

On Characteristics

$\text{R}_{\text{DS(ON)}}$	Static Drain-Source On-Resistance	$V_{\text{GS}}=10\text{V}$, $I_D=8\text{A}$	---	100	130	$\text{m}\Omega$
$V_{\text{GS(th)}}$	Gate Threshold Voltage	$V_{\text{GS}}=V_{\text{DS}}$, $I_D=250\mu\text{A}$	3	4	5	V
gfs	Forward Transconductance	$V_{\text{DS}}=30\text{V}$, $I_D=5\text{A}$	---	6.5	---	S

Dynamic and switching Characteristics

Q_g	Total Gate Charge ^{3,4}	$V_{\text{DS}}=160\text{V}$, $V_{\text{GS}}=10\text{V}$, $I_D=10\text{A}$	---	30	60	nC
Q_{gs}	Gate-Source Charge ^{3,4}		---	7.5	115	
Q_{gd}	Gate-Drain Charge ^{3,4}		---	11	22	
$T_{\text{d(on)}}$	Turn-On Delay Time ^{3,4}	$V_{\text{DD}}=100\text{V}$, $V_{\text{GS}}=10\text{V}$, $R_G=6\Omega$ $I_D=10\text{A}$	---	22	44	ns
T_r	Rise Time ^{3,4}		---	4	8	
$T_{\text{d(off)}}$	Turn-Off Delay Time ^{3,4}		---	44	88	
T_f	Fall Time ^{3,4}		---	6	12	
C_{iss}	Input Capacitance	$V_{\text{DS}}=25\text{V}$, $V_{\text{GS}}=0\text{V}$, $F=1\text{MHz}$	---	1330	2600	pF
C_{oss}	Output Capacitance		---	195	380	
C_{rss}	Reverse Transfer Capacitance		---	21	40	
R_g	Gate resistance	$V_{\text{GS}}=0\text{V}$, $V_{\text{DS}}=0\text{V}$, $F=1\text{MHz}$	---	2.3	4.6	Ω

Drain-Source Diode Characteristics and Maximum Ratings

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I_s	Continuous Source Current	$V_G=V_D=0\text{V}$, Force Current	---	---	20	A
I_{SM}	Pulsed Source Current		---	---	40	A
V_{SD}	Diode Forward Voltage	$V_{\text{GS}}=0\text{V}$, $I_s=1\text{A}$, $T_J=25^\circ\text{C}$	---	---	1	V
t_{rr}	Reverse Recovery Time ³		---	125	---	ns
Q_{rr}	Reverse Recovery Charge ³	$T_J=25^\circ\text{C}$	---	610	---	nC

Note :

1. Repetitive Rating : Pulsed width limited by maximum junction temperature.
2. $V_{\text{DD}}=50\text{V}$, $V_{\text{GS}}=10\text{V}$, $L=1\text{mH}$, $I_{\text{AS}}=29\text{A}$, $R_G=25\Omega$, Starting $T_J=25^\circ\text{C}$.
3. The data tested by pulsed , pulse width $\leq 300\text{us}$, duty cycle $\leq 2\%$.
4. Essentially independent of operating temperature.

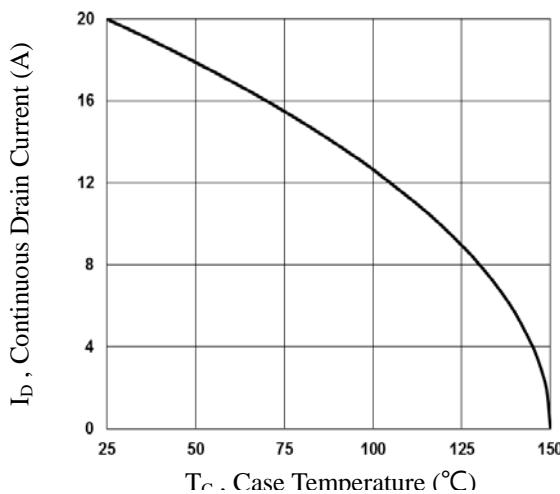


Fig.1 Continuous Drain Current vs. T_C

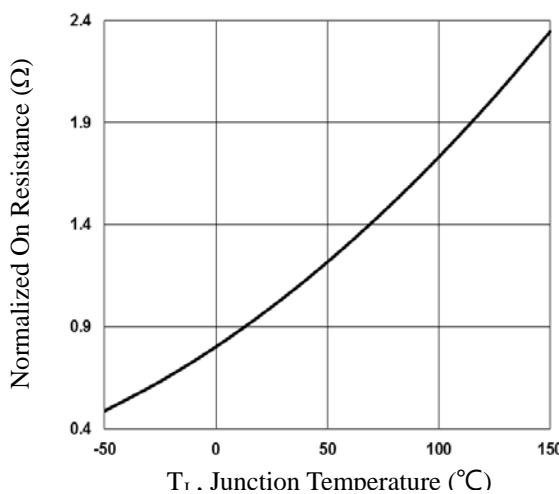


Fig.2 Normalized $R_{DS(on)}$ vs. T_J

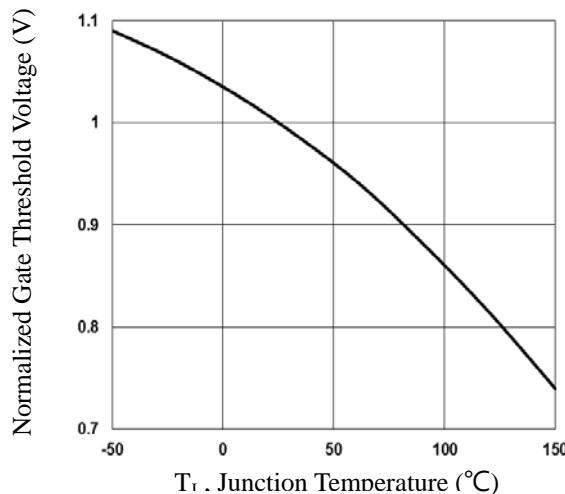


Fig.3 Normalized V_{th} vs. T_J

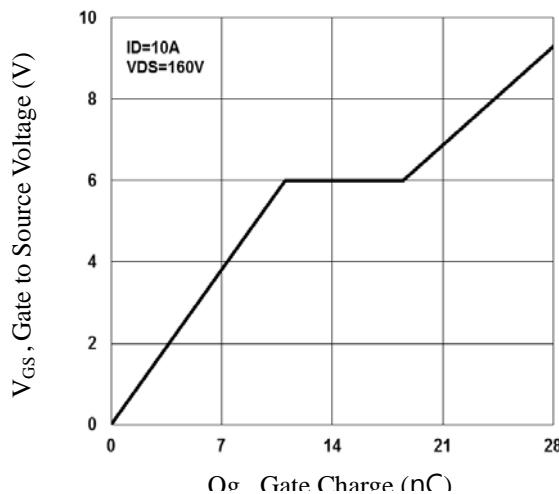


Fig.4 Gate Charge Waveform

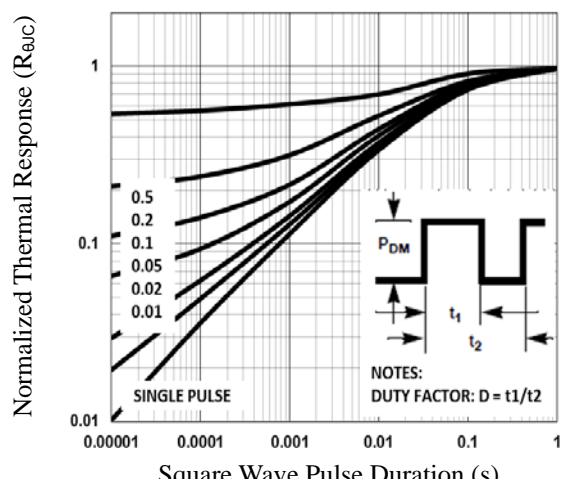


Fig.5 Normalized Transient Impedance

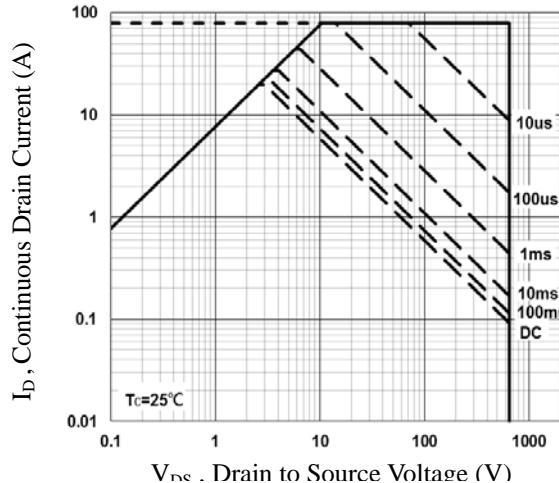


Fig.6 Maximum Safe Operation Area

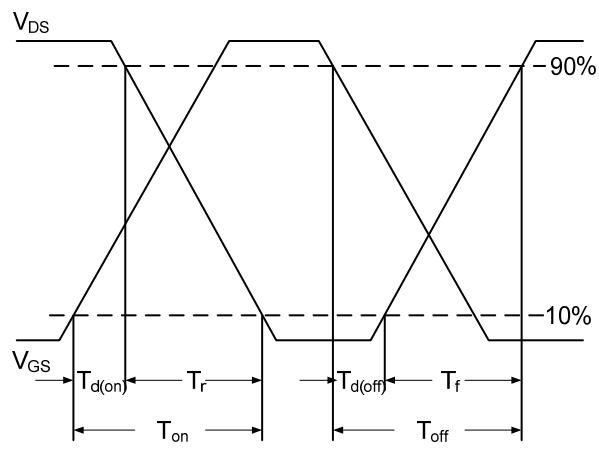


Fig. 7 Switching Time Waveform

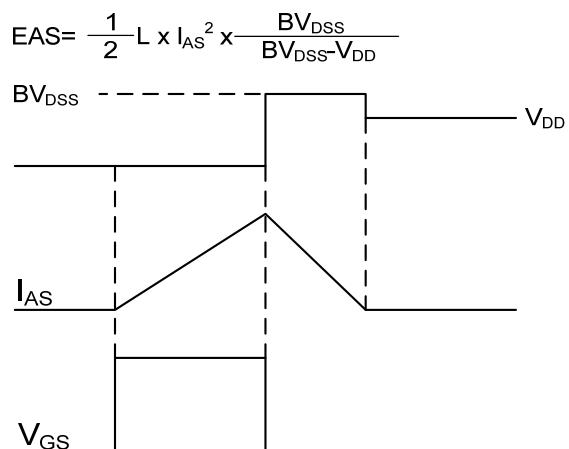
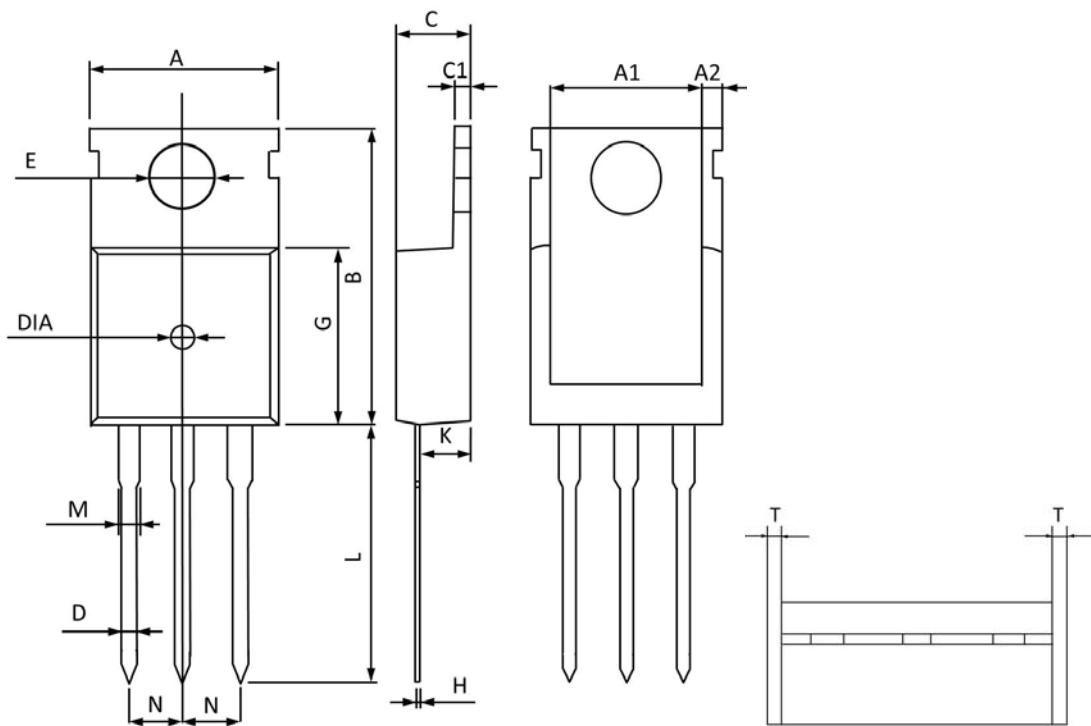


Fig. 8 EAS Waveform



TO220 PACKAGE INFORMATION



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	9.70	10.30	0.382	0.405
A1	8.44	8.84	0.333	0.348
A2	1.05	1.25	0.042	0.049
B	15.40	16.20	0.607	0.637
C	4.28	4.68	0.169	0.184
C1	1.10	1.50	0.044	0.059
D	0.60	1.00	0.024	0.039
E	3.40	3.80	0.134	0.149
G	8.70	9.30	0.343	0.366
H	0.40	0.60	0.016	0.023
K	2.10	2.70	0.083	0.106
L	12.80	13.60	0.504	0.535
L1	2.70	3.30	0.107	0.129
M	1.10	1.50	0.044	0.059
N	2.49	2.59	0.099	0.101
T	0.345	0.355	0.014	0.014
DIA	1.45	1.55	0.058	0.061