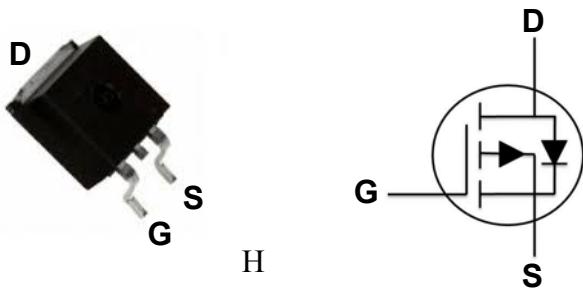


General Description

These P-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

TO252 Pin Configuration



BVDSS	RDS(ON)	ID
-60V	22mΩ	-50A

Features

- -60V,-50A, RDS(ON) 22mΩ@VGS = -10V
- Improved dv/dt capability
- Fast switching
- 100% EAS Guaranteed
- Green Device Available

Applications

- Networking
- Load Switch
- LED applications



Absolute Maximum Ratings ($T_c=25^\circ\text{C}$ unless otherwise noted)

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	-60	V
V_{GS}	Gate-Source Voltage	± 20	V
I_D	Drain Current – Continuous ($T_c=25^\circ\text{C}$)	-50	A
	Drain Current – Continuous ($T_c=100^\circ\text{C}$)	-31	A
I_{DM}	Drain Current – Pulsed ¹	-200	A
EAS	Single Pulse Avalanche Energy ²	245	mJ
IAS	Single Pulse Avalanche Current ²	-70	A
P_D	Power Dissipation ($T_c=25^\circ\text{C}$)	102	W
	Power Dissipation – Derate above 25°C	0.82	W/°C
T_{STG}	Storage Temperature Range	-55 to 150	°C
T_J	Operating Junction Temperature Range	-55 to 150	°C

Thermal Characteristics

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JC}$	Thermal Resistance Junction to Case	---	1.22	°C/W
$R_{\theta JA}$	Thermal Resistance Junction to ambient	---	62	°C/W

Electrical Characteristics ($T_J=25^\circ\text{C}$, unless otherwise noted)

Off Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}$, $I_D=-250\mu\text{A}$	-100	---	---	V
I_{DSS}	Drain-Source Leakage Current	$V_{\text{DS}}=-60\text{V}$, $V_{\text{GS}}=0\text{V}$, $T_J=25^\circ\text{C}$	---	---	-1	μA
		$V_{\text{DS}}=-48\text{V}$, $V_{\text{GS}}=0\text{V}$, $T_J=85^\circ\text{C}$	---	---	-10	μA
I_{GSS}	Gate-Source Leakage Current	$V_{\text{GS}}=\pm 20\text{V}$, $V_{\text{DS}}=0\text{V}$	---	---	± 100	nA

On Characteristics

$R_{\text{DS(ON)}}$	Static Drain-Source On-Resistance	$V_{\text{GS}}=-10\text{V}$, $I_D=-15\text{A}$	---	18	22	$\text{m}\Omega$
		$V_{\text{GS}}=-6\text{V}$, $I_D=-8\text{A}$	---	30	40	$\text{m}\Omega$
$V_{\text{GS(th)}}$	Gate Threshold Voltage	$V_{\text{GS}}=V_{\text{DS}}$, $I_D = -250\mu\text{A}$	-2	-3	-4	V
g_{fs}	Forward Transconductance	$V_{\text{DS}}=-10\text{V}$, $I_D=-5\text{A}$	---	10	---	S

Dynamic and switching Characteristics

Q_g	Total Gate Charge ^{3,4}	$V_{\text{DS}}=-48\text{V}$, $V_{\text{GS}}=-10\text{V}$, $I_D=-10\text{A}$	---	37.2	55	nC
Q_{gs}	Gate-Source Charge ^{3,4}		---	10.4	15	
Q_{gd}	Gate-Drain Charge ^{3,4}		---	10.5	16	
$T_{\text{d(on)}}$	Turn-On Delay Time ^{3,4}	$V_{\text{DD}}=-48\text{V}$, $V_{\text{GS}}=-10\text{V}$, $R_G=25\Omega$ $I_D=5\text{A}$	---	23.1	46	ns
T_r	Rise Time ^{3,4}		---	76.2	150	
$T_{\text{d(off)}}$	Turn-Off Delay Time ^{3,4}		---	113.5	220	
T_f	Fall Time ^{3,4}		---	28.6	56	
C_{iss}	Input Capacitance	$V_{\text{DS}}=-25\text{V}$, $V_{\text{GS}}=0\text{V}$, $F=1\text{MHz}$	---	2165	3200	pF
C_{oss}	Output Capacitance		---	318	480	
C_{rss}	Reverse Transfer Capacitance		---	102	150	

Drain-Source Diode Characteristics and Maximum Ratings

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I_s	Continuous Source Current	$V_G=V_D=0\text{V}$, Force Current	---	---	-50	A
			---	---	-100	A
V_{SD}	Diode Forward Voltage	$V_{\text{GS}}=0\text{V}$, $I_s=-1\text{A}$, $T_J=25^\circ\text{C}$	---	---	-1	V

Note :

1. Repetitive Rating : Pulsed width limited by maximum junction temperature.
2. $V_{\text{DD}}=-30\text{V}$, $V_{\text{GS}}=-10\text{V}$, $L=0.1\text{mH}$, $I_{\text{AS}}=-70\text{A}$, Starting $T_J=25^\circ\text{C}$
3. The data tested by pulsed , pulse width $\leq 300\text{us}$, duty cycle $\leq 2\%$.
4. Essentially independent of operating temperature.

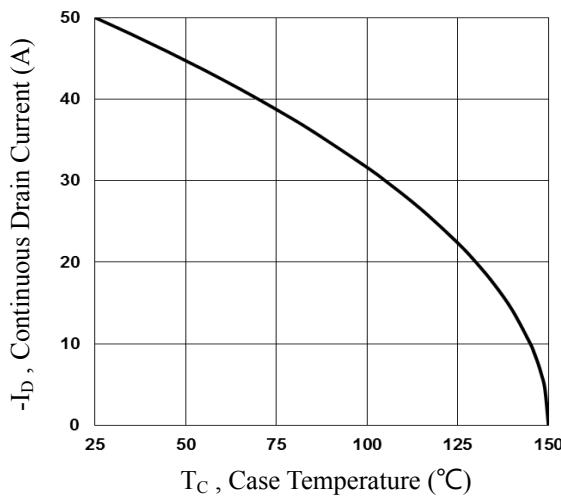


Fig.1 Continuous Drain Current vs. T_C

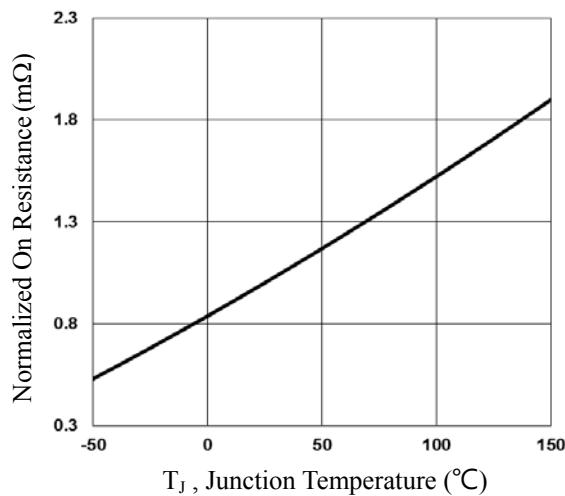


Fig.2 Normalized RD_{SON} vs. T_J

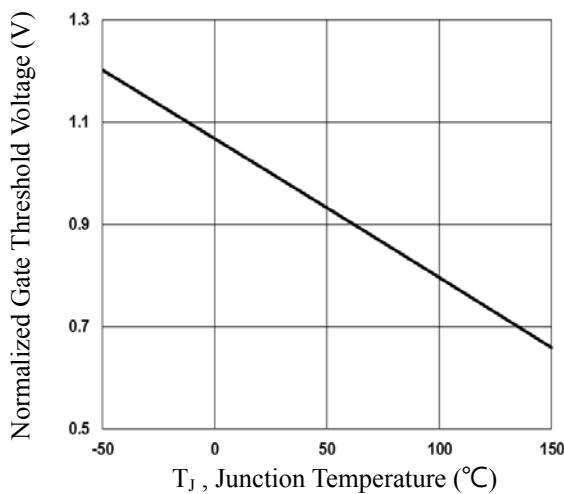


Fig.3 Normalized V_{th} vs. T_J

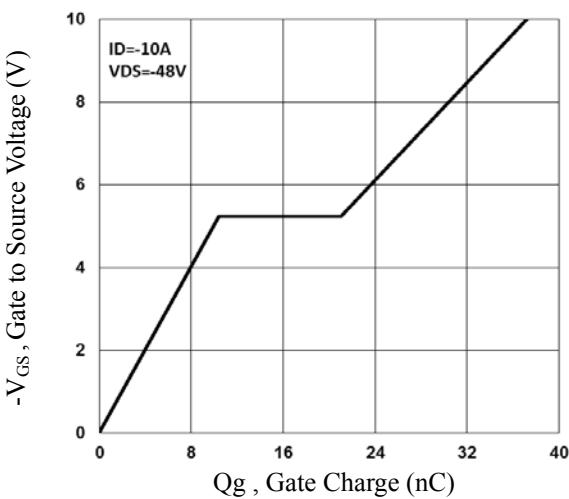


Fig.4 Gate Charge Waveform

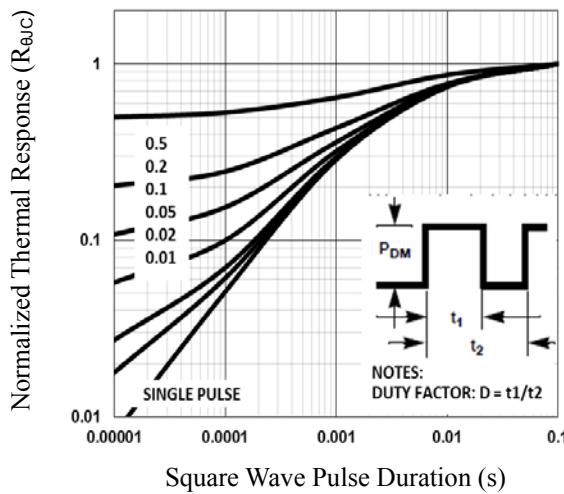


Fig.5 Normalized Transient Impedance

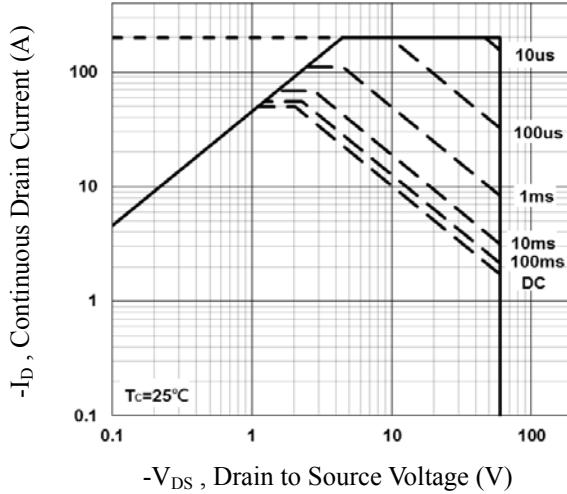


Fig.6 Maximum Safe Operation Area

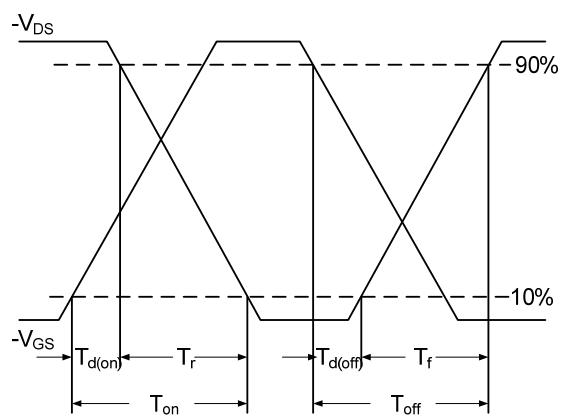


Fig.7 Switching Time Waveform

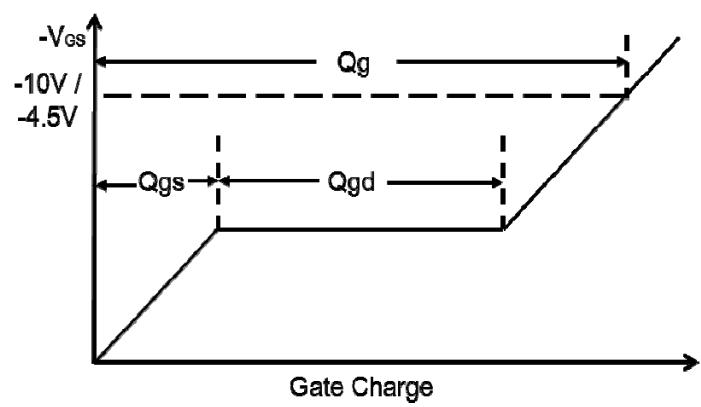
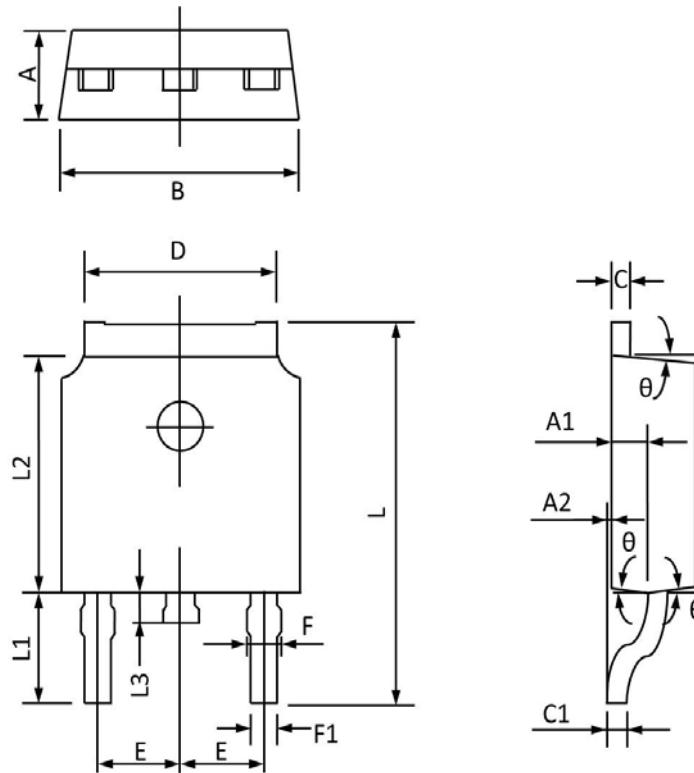


Fig.8 Gate Charge Waveform



TO252 PACKAGE INFORMATION



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	MAX	MIN	MAX	MIN
A	2.400	2.200	0.094	0.087
A1	1.110	0.910	0.044	0.036
A2	0.150	0.000	0.006	0.000
B	6.800	6.400	0.268	0.252
C	0.580	0.450	0.023	0.018
C1	0.580	0.460	0.023	0.018
D	5.500	5.100	0.217	0.201
E	2.386	2.186	0.094	0.086
F	0.940	0.600	0.037	0.024
F1	0.860	0.500	0.034	0.020
L	10.400	9.400	0.409	0.370
L1	3.000	2.400	0.118	0.094
L2	6.200	5.400	0.244	0.213
L3	1.200	0.600	0.047	0.024
θ	9°	3°	9°	3°