

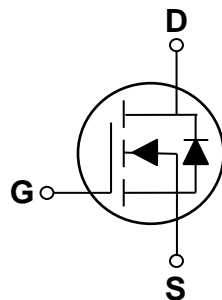
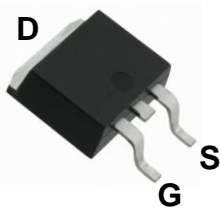
**General Description**

These N-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

BVDSS	RDSON	ID
40V	30mΩ	23A

**Features**

- 40V,23A,  $R_{DS(ON)} = 30m\Omega @ V_{GS} = 10V$
- Improved  $dv/dt$  capability
- Fast switching
- 100% EAS Guaranteed
- 100% PB free and Green Device Available

**TO252 Pin Configuration**

**Applications**

- Networking
- Load Switch
- LED applications


**Absolute Maximum Ratings**  $T_c=25^\circ\text{C}$  unless otherwise noted

Symbol	Parameter	Rating	Units
$V_{DS}$	Drain-Source Voltage	40	V
$V_{GS}$	Gate-Source Voltage	$\pm 20$	V
$I_D$	Drain Current – Continuous ( $T_c=25^\circ\text{C}$ )	23	A
	Drain Current – Continuous ( $T_c=100^\circ\text{C}$ )	14.5	A
$I_{DM}$	Drain Current – Pulsed <sup>1</sup>	92	A
$P_D$	Power Dissipation ( $T_c=25^\circ\text{C}$ )	25	W
	Power Dissipation – Derate above $25^\circ\text{C}$	0.2	W/ $^\circ\text{C}$
$T_{STG}$	Storage Temperature Range	-50 to 150	$^\circ\text{C}$
$T_J$	Operating Junction Temperature Range	-50 to 150	$^\circ\text{C}$

**Thermal Characteristics**

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction to ambient	---	62	$^\circ\text{C}/\text{W}$
$R_{\theta JC}$	Thermal Resistance Junction to Case	---	5	$^\circ\text{C}/\text{W}$

**Electrical Characteristics (T<sub>J</sub>=25 °C, unless otherwise noted)**
**Off Characteristics**

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V, I <sub>D</sub> =250μA	100	---	---	V
I <sub>DSS</sub>	Drain-Source Leakage Current	V <sub>DS</sub> =40V, V <sub>GS</sub> =0V, T <sub>J</sub> =25°C	---	---	1	μA
		V <sub>DS</sub> =32V, V <sub>GS</sub> =0V, T <sub>J</sub> =125°C	---	---	10	μA
I <sub>GSS</sub>	Gate-Source Leakage Current	V <sub>GS</sub> =±20V, V <sub>DS</sub> =0V	---	---	±100	nA

**On Characteristics**

R <sub>DS(ON)</sub>	Static Drain-Source On-Resistance	V <sub>GS</sub> =10V, I <sub>D</sub> =12A	---	25	30	mΩ
		V <sub>GS</sub> =4.5V, I <sub>D</sub> =10A	---	32	40	mΩ
V <sub>GS(th)</sub>	Gate Threshold Voltage	V <sub>GS</sub> =V <sub>DS</sub> , I <sub>D</sub> =250μA	1.2	1.6	2.5	V
g <sub>fs</sub>	Forward Transconductance	V <sub>DS</sub> =10V, I <sub>D</sub> =3A	---	9	---	S

**Dynamic and switching Characteristics**

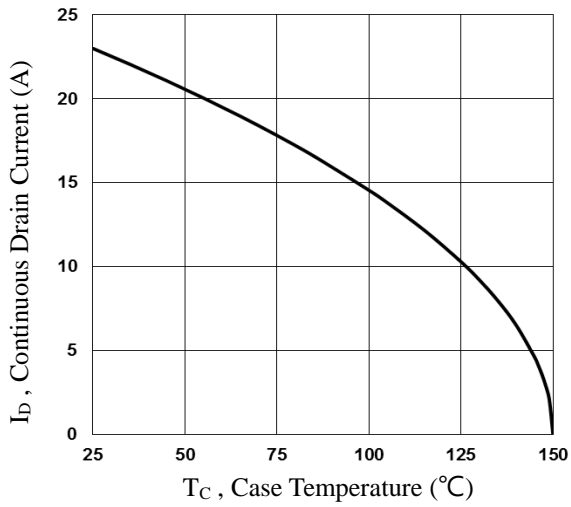
Q <sub>g</sub>	Total Gate Charge <sup>2,3</sup>	V <sub>DS</sub> =20V, V <sub>GS</sub> =4.5V, I <sub>D</sub> =5A	---	4.5	10	nC
Q <sub>gs</sub>	Gate-Source Charge <sup>2,3</sup>		---	1.5	2.4	
Q <sub>gd</sub>	Gate-Drain Charge <sup>2,3</sup>		---	1.9	5	
T <sub>d(on)</sub>	Turn-On Delay Time <sup>2,3</sup>	V <sub>DD</sub> =20V, V <sub>GS</sub> =4.5V, R <sub>G</sub> =25Ω I <sub>D</sub> =1A	---	3.2	6	ns
T <sub>r</sub>	Rise Time <sup>2,3</sup>		---	8.6	16	
T <sub>d(off)</sub>	Turn-Off Delay Time <sup>2,3</sup>		---	18	36	
T <sub>f</sub>	Fall Time <sup>2,3</sup>		---	6	12	
C <sub>iss</sub>	Input Capacitance	V <sub>DS</sub> =25V, V <sub>GS</sub> =0V, F=1MHz	---	420	800	pF
C <sub>oss</sub>	Output Capacitance		---	65	120	
C <sub>rss</sub>	Reverse Transfer Capacitance		---	40	80	
R <sub>g</sub>	Gate resistance	V <sub>GS</sub> =0V, V <sub>DS</sub> =0V, F=1MHz	---	1.2	2.4	Ω

**Drain-Source Diode Characteristics and Maximum Ratings**

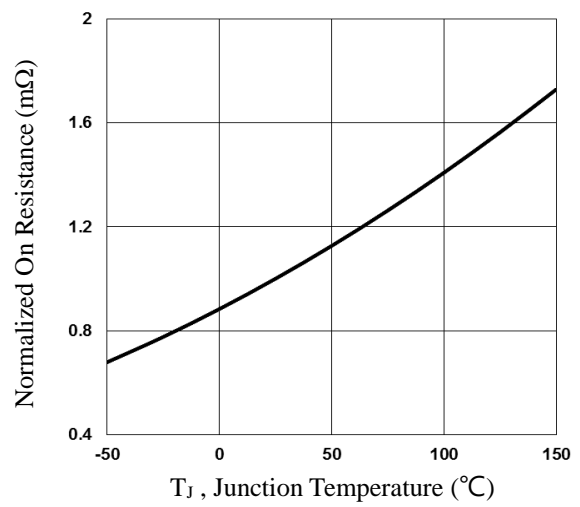
Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I <sub>S</sub>	Continuous Source Current	V <sub>G</sub> =V <sub>D</sub> =0V, Force Current	---	---	23	A
I <sub>SM</sub>	Pulsed Source Current		---	---	46	A
V <sub>SD</sub>	Diode Forward Voltage	V <sub>GS</sub> =0V, I <sub>S</sub> =1A, T <sub>J</sub> =25°C	---	---	1	V
t <sub>rr</sub>	Reverse Recovery Time	V <sub>GS</sub> =0V, I <sub>S</sub> =12A, di/dt=100A/μs	---	4.5	---	ns
Q <sub>rr</sub>	Reverse Recovery Charge	T <sub>J</sub> =25°C	---	0.8	---	nC

Note :

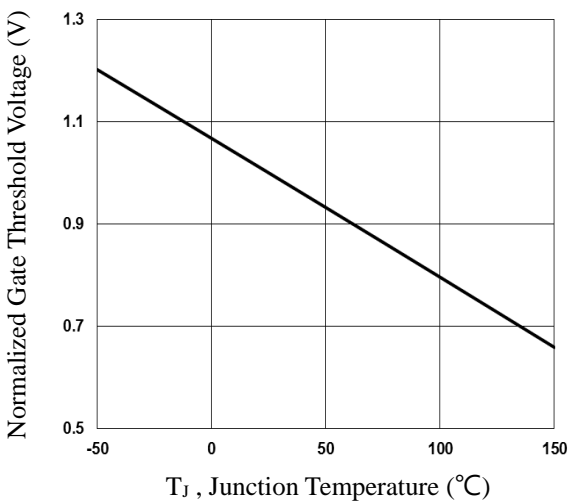
1. Repetitive Rating : Pulsed width limited by maximum junction temperature.
2. The data tested by pulsed, pulse width ≤ 300us, duty cycle ≤ 2%.
3. Essentially independent of operating temperature.



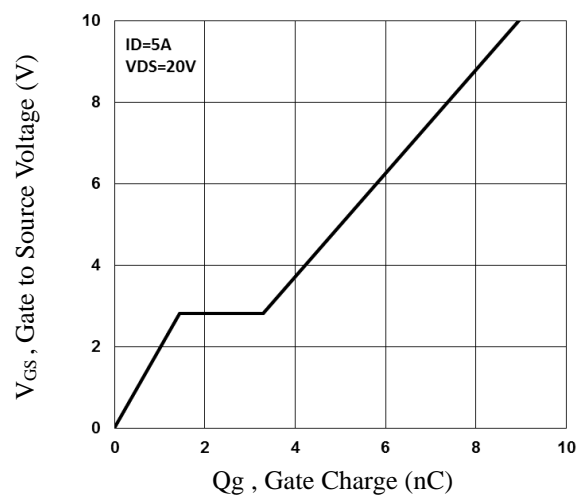
**Fig.1 Continuous Drain Current vs. TC**



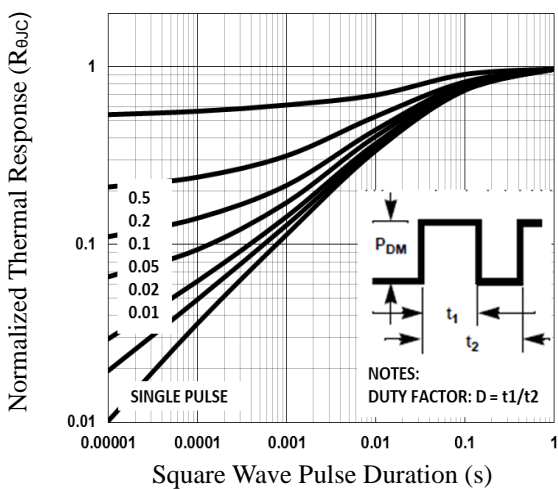
**Fig.2 Normalized RDSON vs. TJ**



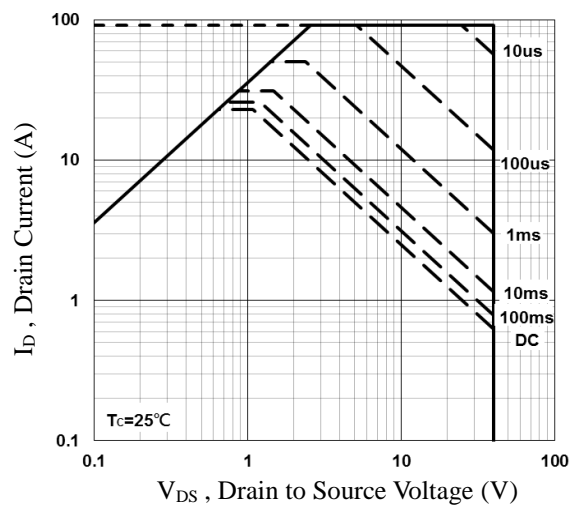
**Fig.3 Normalized V<sub>th</sub> vs. TJ**



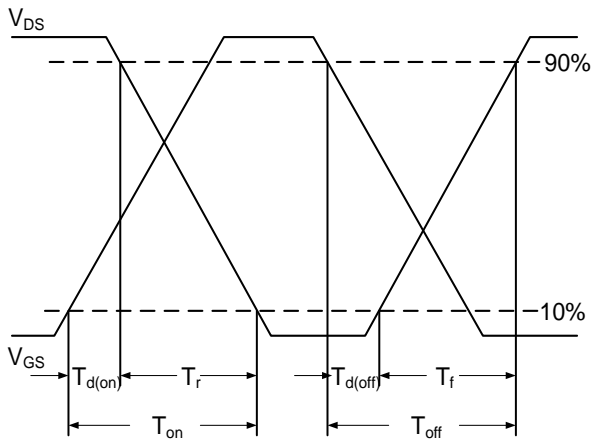
**Fig.4 Gate Charge Characteristics**



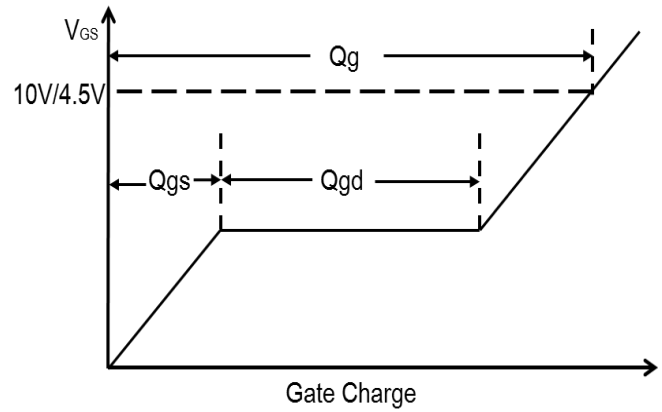
**Fig.5 Normalized Transient Impedance**



**Fig.6 Maximum Safe Operation Area**



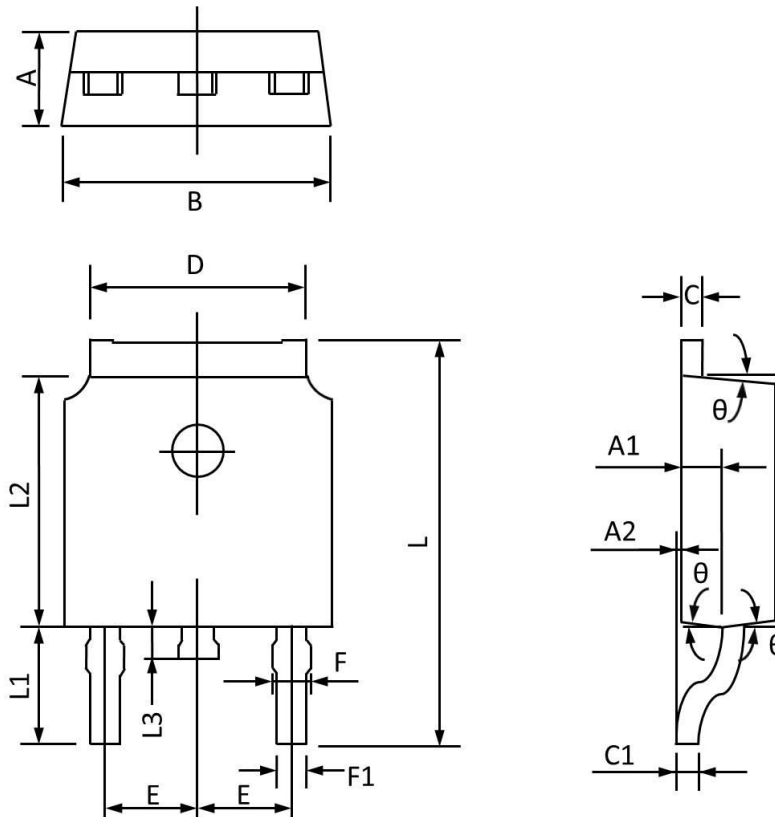
**Fig.7 Switching Time Waveform**



**Fig.8 Gate Charge Waveform**



**TO252 PACKAGE INFORMATION**



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	2.20	2.40	0.087	0.094
A1	0.91	1.11	0.036	0.044
A2	0.00	0.15	0.000	0.006
B	6.50	6.70	0.256	0.264
C	0.46	0.580	0.018	0.230
C1	0.46	0.580	0.018	0.030
D	5.10	5.46	0.201	0.215
E	2.186	2.386	0.086	0.094
F	0.74	0.94	0.029	0.037
F1	0.660	0.860	0.026	0.034
L	9.80	10.40	0.386	0.409
L1	2.9REF		0.114REF	
L2	6.00	6.20	0.236	0.244
L3	0.60	1.00	0.024	0.039
$\theta$	3°	9°	3°	9°