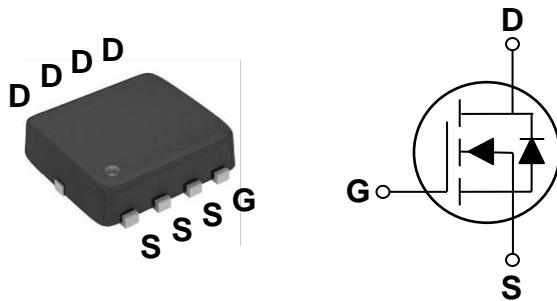


### General Description

These N-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

### PPAK3X3 Pin Configuration



BVDSS	RDSON	ID
60V	15mΩ	35A

### Features

- 60V,35A,  $RDS(ON) = 15m\Omega$  @ $VGS = 10V$
- Improved dv/dt capability
- Fast switching
- 100% EAS Guaranteed
- Green Device Available

### Applications

- Motor Drive
- Power Tools
- LED Lighting
- Quick Charger



### Absolute Maximum Ratings $T_c=25^\circ C$ unless otherwise noted

Symbol	Parameter	Rating	Units
$V_{DS}$	Drain-Source Voltage	60	V
$V_{GS}$	Gate-Source Voltage	$\pm 20$	V
$I_D$	Drain Current – Continuous ( $T_c=25^\circ C$ )	35	A
	Drain Current – Continuous ( $T_c=100^\circ C$ )	22	A
$I_{DM}$	Drain Current – Pulsed <sup>1</sup>	140	A
EAS	Single Pulse Avalanche Energy <sup>2</sup>	45	mJ
IAS	Single Pulse Avalanche Current <sup>2</sup>	30	A
$P_D$	Power Dissipation ( $T_c=25^\circ C$ )	46	W
	Power Dissipation – Derate above 25°C	0.37	W/°C
$T_{STG}$	Storage Temperature Range	-55 to 150	°C
$T_J$	Operating Junction Temperature Range	-55 to 150	°C

### Thermal Characteristics

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction to ambient	---	62	°C/W
$R_{\theta JC}$	Thermal Resistance Junction to Case	---	2.7	°C/W

**Electrical Characteristics ( $T_J=25\text{ }^\circ\text{C}$ , unless otherwise noted)**
**Off Characteristics**

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$BV_{DSS}$	Drain-Source Breakdown Voltage	$V_{GS}=0\text{V}$ , $I_D=250\mu\text{A}$	60	---	---	V
$I_{DSS}$	Drain-Source Leakage Current	$V_{DS}=60\text{V}$ , $V_{GS}=0\text{V}$ , $T_J=25\text{ }^\circ\text{C}$	---	---	1	$\mu\text{A}$
		$V_{DS}=48\text{V}$ , $V_{GS}=0\text{V}$ , $T_J=125\text{ }^\circ\text{C}$	---	---	10	$\mu\text{A}$
$I_{GSS}$	Gate-Source Leakage Current	$V_{GS}=\pm 20\text{V}$ , $V_{DS}=0\text{V}$	---	---	$\pm 100$	$\text{nA}$

**On Characteristics**

$R_{DS(\text{ON})}$	Static Drain-Source On-Resistance <sup>3</sup>	$V_{GS}=10\text{V}$ , $I_D=10\text{A}$	---	13	15	$\text{m}\Omega$
		$V_{GS}=4.5\text{V}$ , $I_D=5\text{A}$	---	16	19	$\text{m}\Omega$
$V_{GS(\text{th})}$	Gate Threshold Voltage	$V_{GS}=V_{DS}$ , $I_D=250\mu\text{A}$	1.2	1.7	2.5	V
$g_{fs}$	Forward Transconductance	$V_{DS}=10\text{V}$ , $I_D=3\text{A}$	---	10	---	S

**Dynamic and switching Characteristics**

$Q_g$	Total Gate Charge <sup>3, 4</sup>	$V_{DS}=30\text{V}$ , $V_{GS}=10\text{V}$ , $I_D=10\text{A}$	---	27	54	nC
$Q_{gs}$	Gate-Source Charge <sup>3, 4</sup>		---	4.2	9	
$Q_{gd}$	Gate-Drain Charge <sup>3, 4</sup>		---	6.2	12	
$T_{d(on)}$	Turn-On Delay Time <sup>3, 4</sup>	$V_{DD}=15\text{V}$ , $V_{GS}=10\text{V}$ , $R_G=6\Omega$ $I_D=1\text{A}$	---	8.6	16	ns
$T_r$	Rise Time <sup>3, 4</sup>		---	24.2	48	
$T_{d(off)}$	Turn-Off Delay Time <sup>3, 4</sup>		---	32.3	64	
$T_f$	Fall Time <sup>3, 4</sup>		---	7.9	16	
$C_{iss}$	Input Capacitance	$V_{DS}=25\text{V}$ , $V_{GS}=0\text{V}$ , $F=1\text{MHz}$	---	1515	3000	pF
$C_{oss}$	Output Capacitance		---	120	200	
$C_{rss}$	Reverse Transfer Capacitance		---	76	120	
$R_g$	Gate resistance	$V_{GS}=0\text{V}$ , $V_{DS}=0\text{V}$ , $F=1\text{MHz}$	---	1.8	3.6	$\Omega$

**Drain-Source Diode Characteristics and Maximum Ratings**

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$I_s$	Continuous Source Current	$V_G=V_D=0\text{V}$ , Force Current	---	---	35	A
			---	---	70	A
$V_{SD}$	Diode Forward Voltage <sup>3</sup>	$V_{GS}=0\text{V}$ , $I_s=1\text{A}$ , $T_J=25\text{ }^\circ\text{C}$	---	---	1	V
			---	19	---	ns
$Q_{rr}$	Reverse Recovery Charge	$V_{GS}=0\text{V}$ , $I_s=1\text{A}$ , $di/dt=100\text{A}/\mu\text{s}$ $T_J=25\text{ }^\circ\text{C}$	---	5	---	nC

Note :

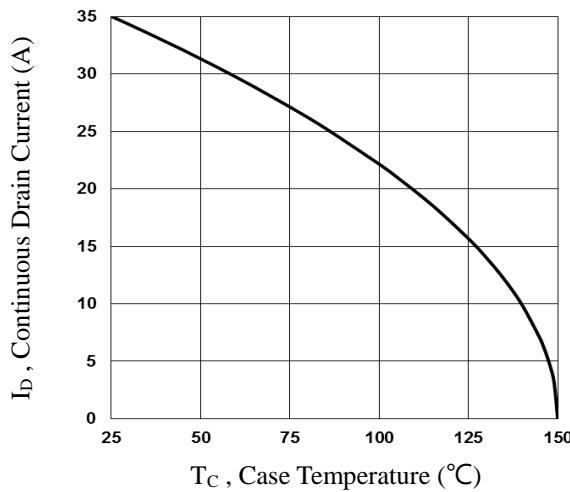
1. Repetitive Rating : Pulsed width limited by maximum junction temperature.
2.  $V_{DD}=25\text{V}$ ,  $V_{GS}=10\text{V}$ ,  $L=0.1\text{mH}$ ,  $I_{AS}=30\text{A}$ ,  $R_G=25\Omega$ , Starting  $T_J=25\text{ }^\circ\text{C}$ .
3. The data tested by pulsed , pulse width  $\leq 300\mu\text{s}$ , duty cycle  $\leq 2\%$ .
4. Essentially independent of operating temperature.



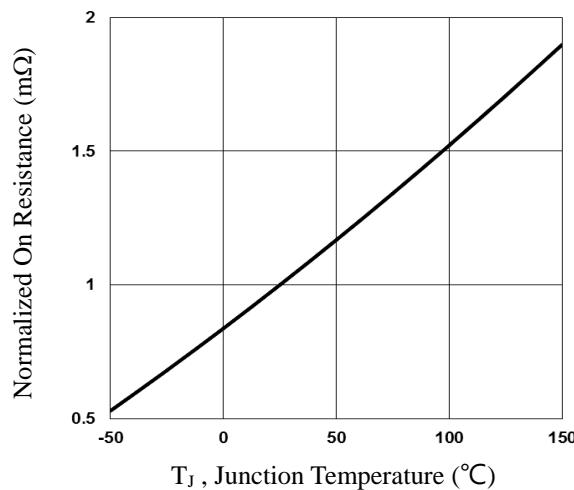
STEIF POWER  
TECHNOLOGY

60V N-Channel MOSFETs

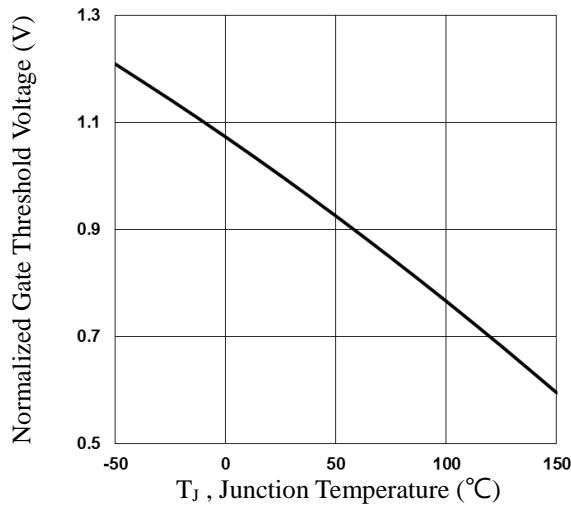
**SPC6964Z**



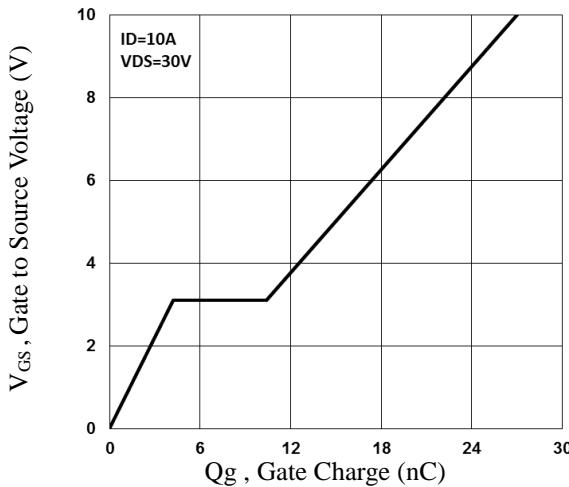
**Fig.1 Continuous Drain Current vs.  $T_C$**



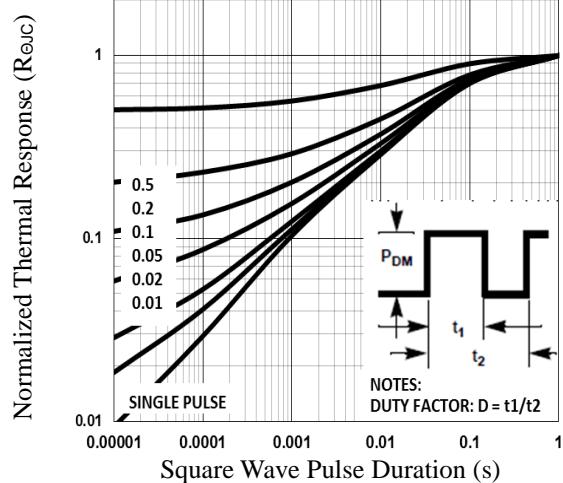
**Fig.2 Normalized  $R_{DS(on)}$  vs.  $T_J$**



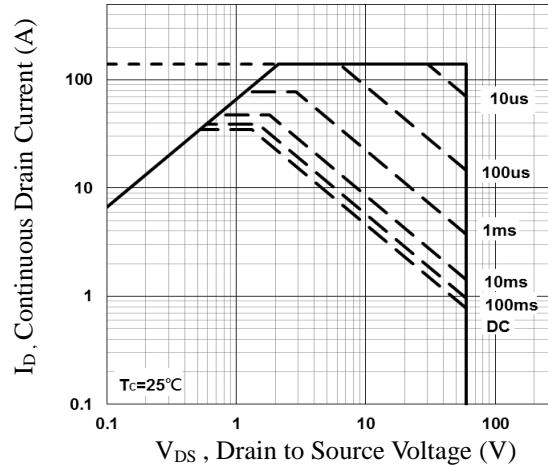
**Fig.3 Normalized  $V_{th}$  vs.  $T_J$**



**Fig.4 Gate Charge Waveform**



**Fig.5 Normalized Transient Response**



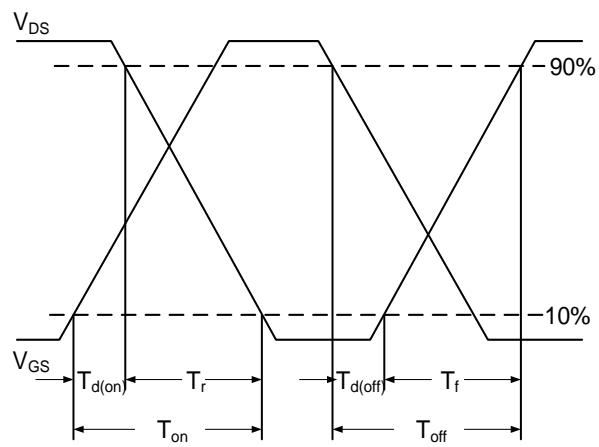
**Fig.6 Maximum Safe Operation Area**



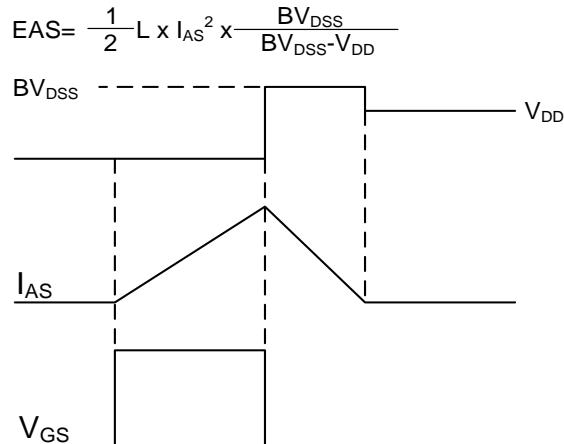
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60V N-Channel MOSFETs

**SPC6964Z**



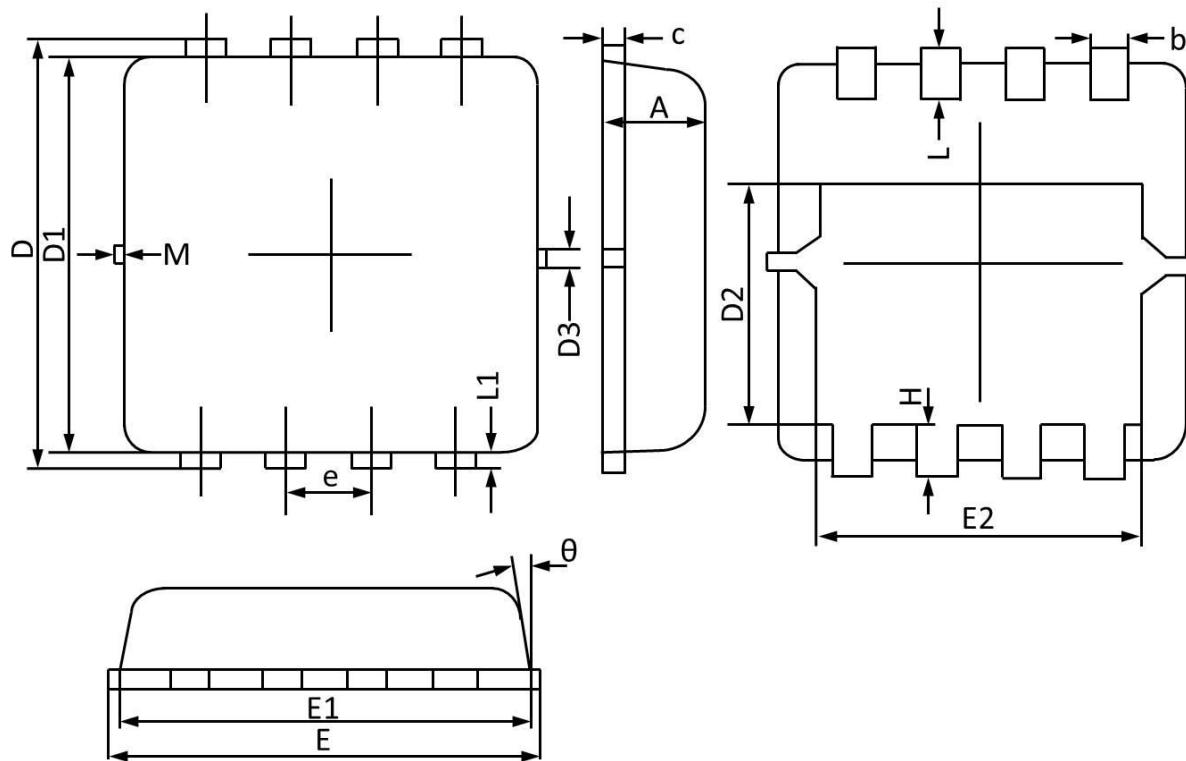
**Fig.7 Switching Time Waveform**



**Fig.8 EAS Waveform**



## PPAK3x3 PACKAGE INFORMATION



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	0.700	0.800	0.028	0.031
b	0.250	0.350	0.010	0.013
c	0.100	0.250	0.004	0.009
D	3.250	3.450	0.128	0.135
D1	3.000	3.200	0.119	0.125
D2	1.780	1.980	0.070	0.077
D3	0.130 REF		0.005 REF	
E	3.200	3.400	0.126	0.133
E1	3.000	3.200	0.119	0.125
E2	2.390	2.590	0.094	0.102
e	0.650 BSC		0.026 BSC	
H	0.300	0.500	0.011	0.019
L	0.300	0.500	0.011	0.019
L1	0.130 REF		0.005 REF	
$\theta$	0°	12°	0°	12°
M	0.150 REF		0.006 REF	