

SN8A - SN8M

Surface Mount Glass Passivated Standard Rectifier Reverse Voltage 50~1000V Forward Current 8A

Features

- · Plastic package has Underwriters Laboratory Flammability Classification 94V
- · Low profile
- · Build-in strain relief, ideal for automated placement
- · Glass passivated chip junction





DO-214AB (SMC)↔

Mechanical Data

- Case: JEDEC DO-214AB (SMC)
- Terminals: solder plated, solderable per MIL-STD-750, Method 2026
- High temperature soldering guaranteed: 260°C/10 seconds at terminals
- · Polarity: color band denotes cathode end

$\begin{tabular}{ll} \textbf{Maximum Ratings} & (T_A=25\,^{\circ}\text{C unless otherwise specified}) \end{tabular}$

Parameter	Symbol	SN8A	SN8B	SN8D	SN8G	SN8J	SN8K	SN8M	Unit
Maximum Repetitive Peak Reverse Voltage	VRRM	50	100	200	400	600	800	1000	V
Maximum RMS Voltage	VRMS	35	70	140	280	420	560	700	V
Maximum DC Blocking Voltage	VDC	50	100	200	400	600	800	1000	V
Maximum Average Forward Rectified Current at TL (See Fig.1)	lf(AV)		8.0						
Peak Forward Surge Current 8.3 ms Single Half Sine-wave Superimposed On Rated Load	IFSM	200							А
Operating Junction and Storage Temperature Range	ТJ, Tsтg	- 55 to + 150							°C

Electrical Characteristics (T_A=25°C unless otherwise specified)

Parameter	Test Conditions	Symbol	SN8A	SN8B	SN8D	SN8G	SN8J	SN8K	SN8M	Unit
Maximum Instantaneous Forward Voltage	8A	V _F				1.1				V
Maximum DC Reverse Current at	T _A =25°C	- _A =25°C		5						
Rated DC Blocking Voltage	T _A =100°C		50							
Typical Thermal Resistance1)	Juntion to Ambient	$R_{ heta JA}$				28				°C/W
Typical Junction Capacitance	4.0 V, 1 MHz	C_{J}				50				pF

Note:1) The thermal resistance from junction to ambient, case or lead, mounted on P.C.B with 30×30mm copper pads



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Ratings and Characteristic Curves

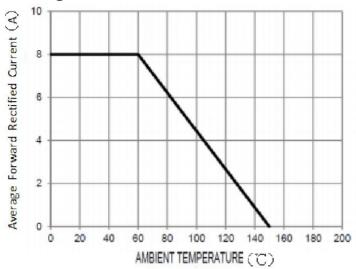


Figure 1. Forward Current Derating Curve

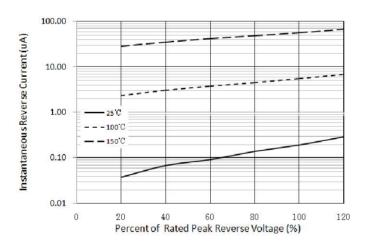


Figure 3. Typical Reverse Characteristics

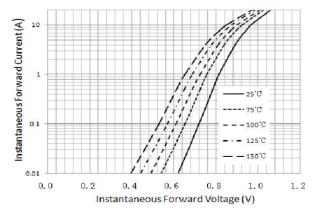


Figure 5. Typical Instantaneous Forward Characteristics

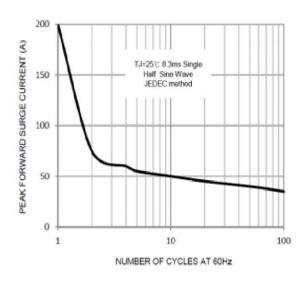


Figure 2. Maximum Non-Repetitive Peak Forward Surge Current

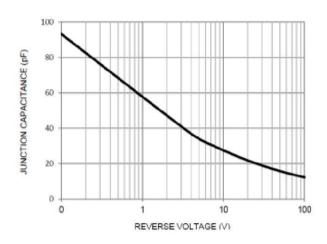


Figure 4. Typical Junction Capacitance



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Package Outline Dimensions

