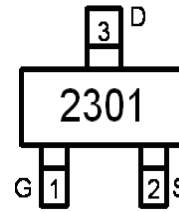


## Main Product Characteristics:

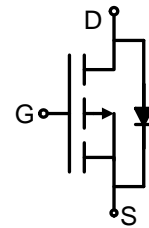
$V_{DS}$	-20V
$R_{DS(on)}$	60m $\Omega$ (typ.)
$I_D$	-3A <sup>①</sup>



SOT-23



Marking and pin  
Assignment



Schematic diagram

## Features and Benefits:

- Advanced MOSFET process technology
- Special designed for PWM, load switching and general purpose applications
- Ultra low on-resistance with low gate charge
- Fast switching and reverse body recovery
- 150°C operating temperature



## Description:

It utilizes the latest processing techniques to achieve the high cell density and reduces the on-resistance with high repetitive avalanche rating. These features combine to make this design an extremely efficient and reliable device for use in power switching application and a wide variety of other applications.

## Absolute max Rating: @ $T_A=25^\circ\text{C}$ unless otherwise specified

Symbol	Parameter	Max.	Units
$I_D$ @ $TC = 25^\circ\text{C}$	Continuous Drain Current, $V_{GS}$ @ 10V	-3 <sup>①</sup>	A
$I_D$ @ $TC = 70^\circ\text{C}$	Continuous Drain Current, $V_{GS}$ @ 10V	-1.8 <sup>①</sup>	
$I_{DM}$	Pulsed Drain Current <sup>②</sup>	-10	
$P_D$ @ $TC = 25^\circ\text{C}$	Power Dissipation <sup>③</sup>	1.25	W
$V_{DS}$	Drain-Source Voltage	-20	V
$V_{GS}$	Gate-to-Source Voltage	$\pm 12$	V
$T_J$ $T_{STG}$	Operating Junction and Storage Temperature Range	-55 to +150	$^\circ\text{C}$

## Thermal Resistance

Symbol	Characterizes	Typ.	Max.	Units
$R_{\theta JA}$	Junction-to-ambient ( $t \leq 10\text{s}$ ) <sup>④</sup>	—	100	$^\circ\text{C}/\text{W}$

## Electrical Characterizes @ $T_A=25^{\circ}\text{C}$ unless otherwise specified

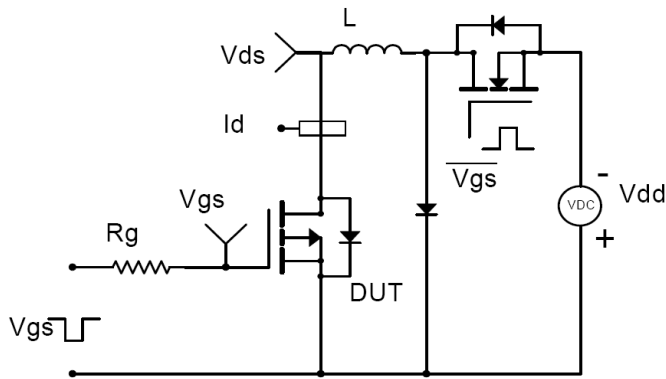
Symbol	Parameter	Min.	Typ.	Max.	Units	Conditions
$V_{(BR)DSS}$	Drain-to-Source breakdown voltage	-20	—	—	V	$V_{GS} = 0V, I_D = -250\mu A$
$R_{DS(on)}$	Static Drain-to-Source on-resistance	—	60	90	m $\Omega$	$V_{GS}=-4.5V, I_D = -3A$
		—	85	115		$V_{GS}=-2.5V, I_D = -2A$
$V_{GS(th)}$	Gate threshold voltage	-0.5	—	-1	V	$V_{DS} = V_{GS}, I_D = -250\mu A$
		—	-0.58	—		$T_J = 125^{\circ}\text{C}$
$I_{DSS}$	Drain-to-Source leakage current	—	—	-1	$\mu A$	$V_{DS} = -20V, V_{GS} = 0V$
		—	—	-50		$T_J = 125^{\circ}\text{C}$
$I_{GSS}$	Gate-to-Source forward leakage	—	—	100	nA	$V_{GS} = 12V$
		—	—	-100		$V_{GS} = -12V$
$Q_g$	Total gate charge	—	9.6	—	nC	$I_D = -3A,$ $V_{DS}=-10V,$ $V_{GS} = -4.5V$
$Q_{gs}$	Gate-to-Source charge	—	1.1	—		
$Q_{gd}$	Gate-to-Drain("Miller") charge	—	2.6	—		
$t_{d(on)}$	Turn-on delay time	—	9.7	—	ns	$V_{GS}=-4.5V, V_{DS}=-20V,$ $R_{GEN}=3\Omega$
$t_r$	Rise time	—	18	—		
$t_{d(off)}$	Turn-Off delay time	—	25	—		
$t_f$	Fall time	—	31	—		
$C_{iss}$	Input capacitance	—	490	—	pF	$V_{GS} = 0V,$ $V_{DS} = -10V,$ $f = 1\text{MHz}$
$C_{oss}$	Output capacitance	—	75	—		
$C_{riss}$	Reverse transfer capacitance	—	60	—		

## Source-Drain Ratings and Characteristics

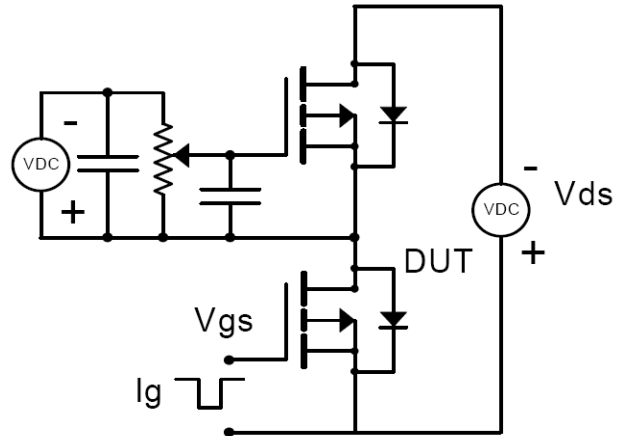
Symbol	Parameter	Min.	Typ.	Max.	Units	Conditions
$I_S$	Continuous Source Current (Body Diode)	—	—	-3 ①	A	MOSFET symbol showing the integral reverse p-n junction diode.
$I_{SM}$	Pulsed Source Current (Body Diode)	—	—	-10	A	
$V_{SD}$	Diode Forward Voltage	—	-0.83	-1.2	V	

## Test circuits and Waveforms

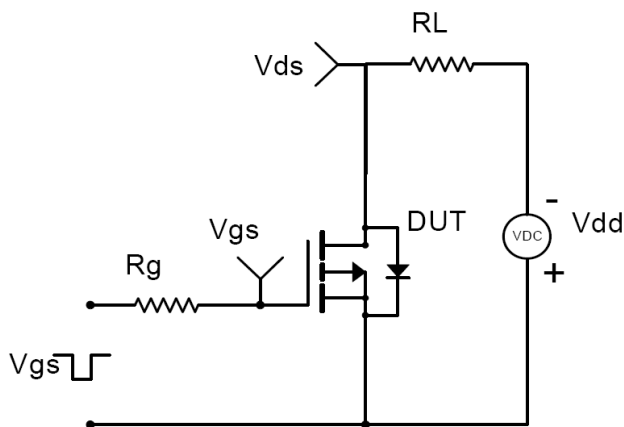
EAS test circuit:



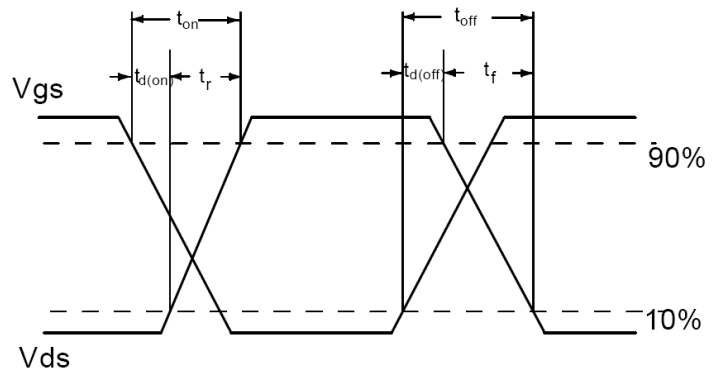
Gate charge test circuit:



Switching time test circuit:



Switch Waveforms:



### Notes:

- ① Calculated continuous current based on maximum allowable junction temperature.
- ② Repetitive rating; pulse width limited by max. junction temperature.
- ③ The power dissipation PD is based on max. junction temperature, using junction-to-case thermal resistance.
- ④ The value of  $R_{\theta JA}$  is measured with the device mounted on 1 in 2 FR-4 board with 2oz. Copper, in a still air environment with  $T_A = 25^\circ\text{C}$

## Typical electrical and thermal characteristics

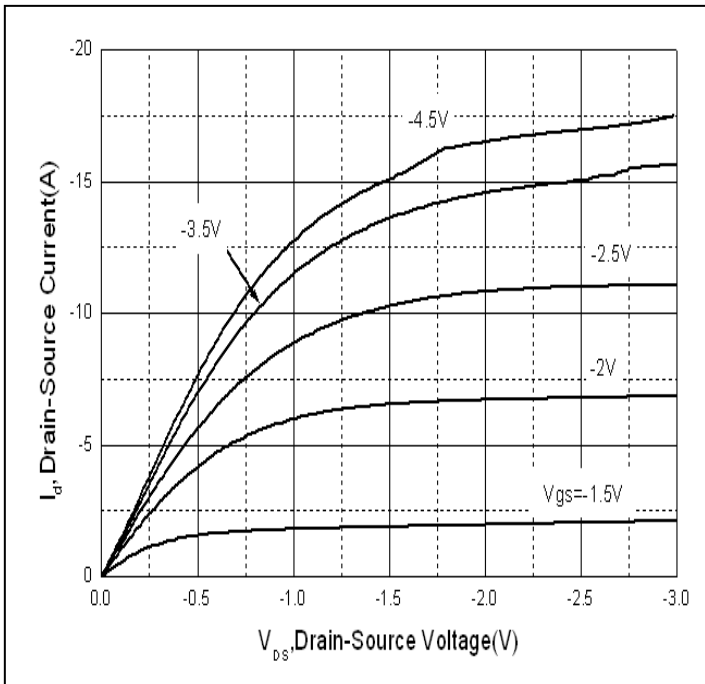


Figure 1: Typical Output Characteristics

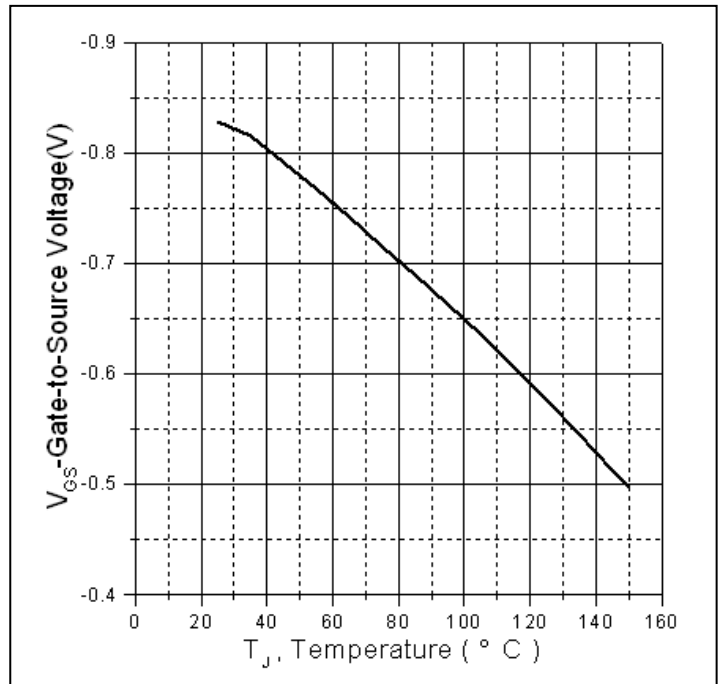


Figure 2. Gate to source cut-off voltage

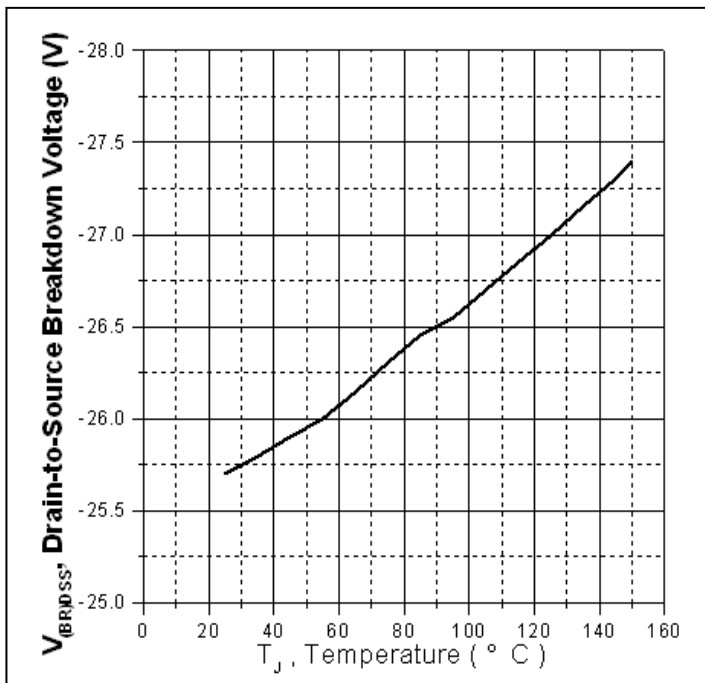


Figure 3. Drain-to-Source Breakdown Voltage Vs. Case Temperature

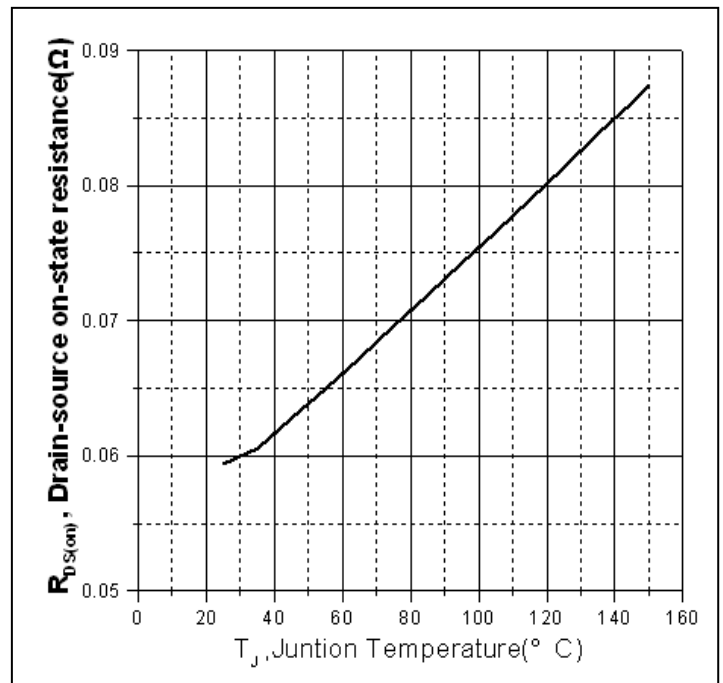


Figure 4: Normalized On-Resistance Vs. Case Temperature

## Typical electrical and thermal characteristics

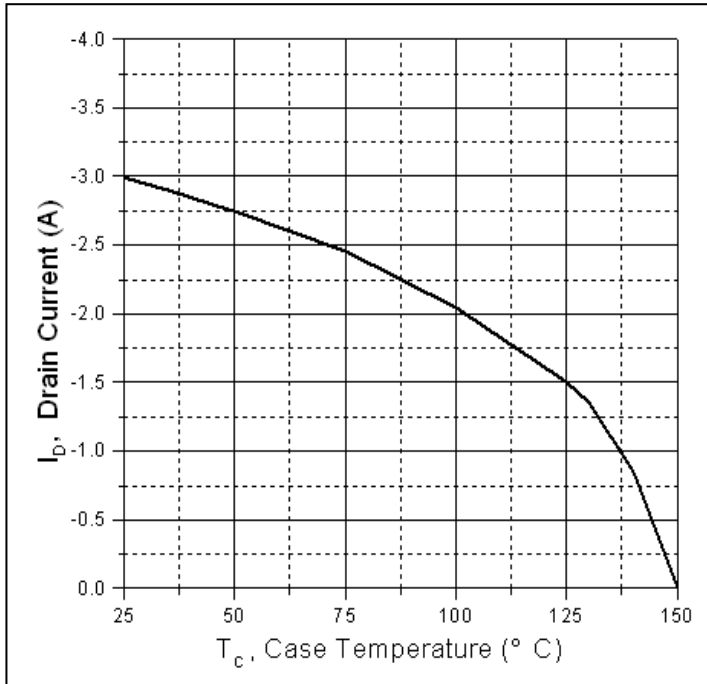


Figure 5. Maximum Drain Current Vs. Case Temperature

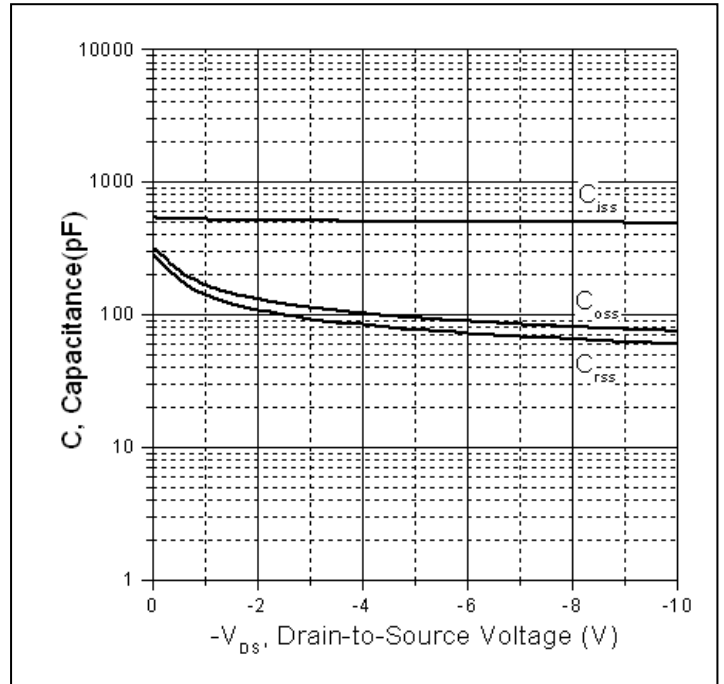


Figure 6. Typical Capacitance Vs. Drain-to-Source Voltage

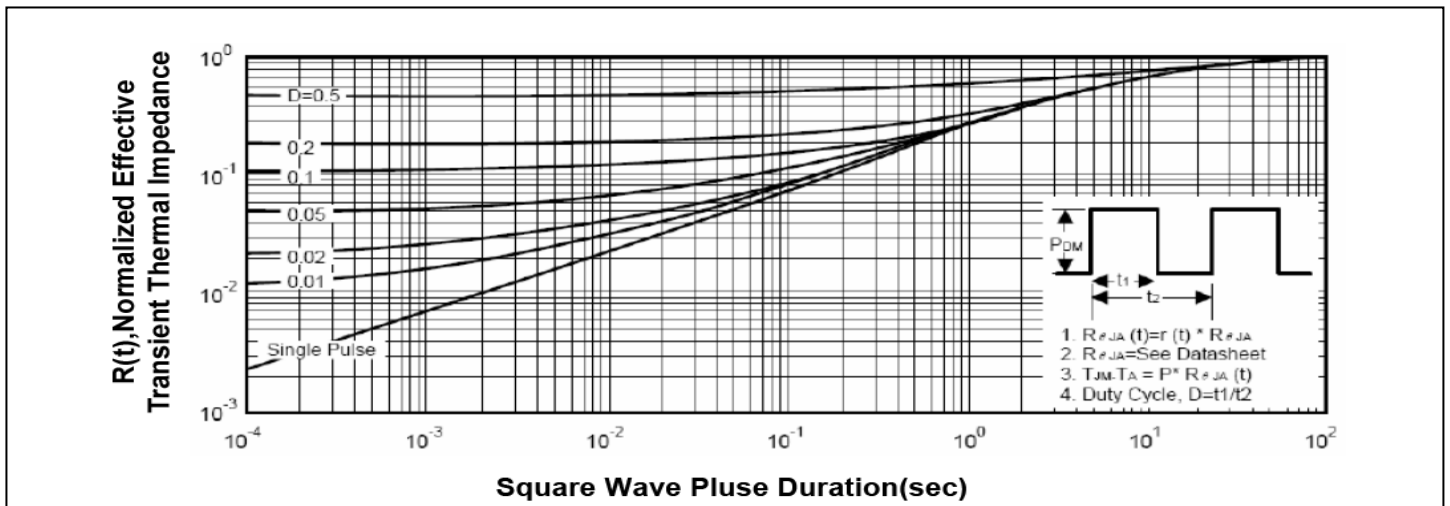
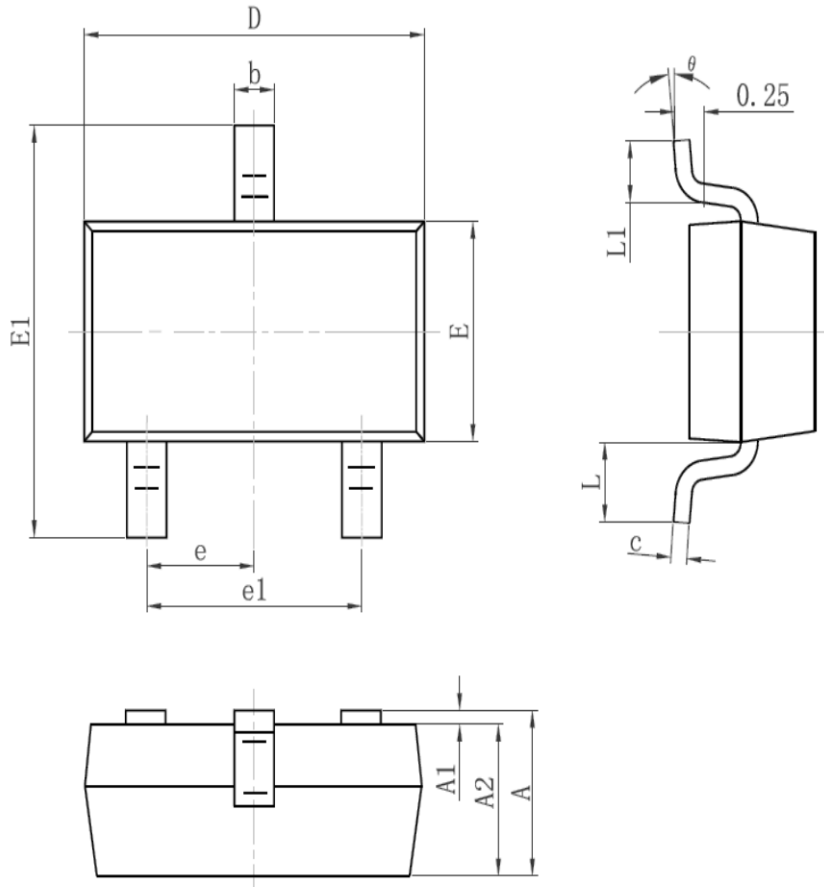


Figure 7. Maximum Effective Transient Thermal Impedance Junction-to-Case



Mechanical Data:

SOT-23 PACKAGE OUTLINE DIMENSION



Symbol	Dimension In Millimeters		Dimension In Inches	
	Min	Max	Min	Max
A	0.900	1.150	0.035	0.045
A1	0.000	0.100	0.000	0.004
A2	0.900	1.050	0.035	0.041
b	0.300	0.500	0.012	0.020
c	0.080	0.150	0.003	0.006
D	2.800	3.000	0.110	0.118
E	1.200	1.400	0.047	0.055
E1	2.250	2.550	0.089	0.100
e	0.95TYP		0.037TYP	
e1	1.800	2.000	0.071	0.079
L	0.55REF		0.022REF	
L1	0.300	0.500	0.012	0.020
θ	0°	8°	0°	8°



**Ordering and Marking Information**

<p><b>Device Marking: 2301</b></p> <p><b>Package (Available)</b> <b>SOT23</b></p> <p><b>Operating Temperature Range</b> <b>C : -55 to 150 °C</b></p>
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**Devices per Unit**

<b>Package Type</b>	<b>Units/Tape</b>	<b>Tapes/Inner Box</b>	<b>Units/Inner Box</b>	<b>Inner Boxes/Carton Box</b>	<b>Units/Carton Box</b>
SOT23	3000	10	30000	4	120000

**Reliability Test Program**

<b>Test Item</b>	<b>Conditions</b>	<b>Duration</b>	<b>Sample Size</b>
<b>High Temperature Reverse Bias(HTRB)</b>	<b>T<sub>j</sub>=125°C to 150°C @ 80% of Max V<sub>DSS</sub>/V<sub>CES</sub>/V<sub>R</sub></b>	<b>168 hours 500 hours 1000 hours</b>	<b>3 lots x 77 devices</b>
<b>High Temperature Gate Bias(HTGB)</b>	<b>T<sub>j</sub>=150°C @ 100% of Max V<sub>GSS</sub></b>	<b>168 hours 500 hours 1000 hours</b>	<b>3 lots x 77 devices</b>

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